

BC857S PNP, Multi-Chip, General-Purpose Amplifier

Description

This device is designed for general-purpose amplifier applications at collector currents to 200 mA. Sourced from Process 68.

Block Diagram



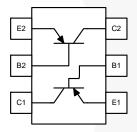


Figure 1. Device Package

Figure 2. Internal Connections

Ordering Information

Part Number	Top Mark	Package	Packing Method
BC857S	.3C	SC70 6L	Tape and Reel

January 2014

Absolute Maximum Ratings^{(1),(2)}

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_A = 25^{\circ}$ C unless otherwise noted.

Symbol	Parameter	Value	Unit
V _{CEO}	Collector-Emitter Voltage	-45	V
V _{CES}	Collector-Base Voltage	-50	V
V _{CBO}	Collector-Base Voltage	-50	V
V _{EBO}	Emitter-Base Voltage	-5.0	V
Ι _C	Collector Current - Continuous	-200	mA
T _J , T _{STG}	Operating and Storage Junction Temperature Range	-55 to +150	°C

Notes:

- 1. These ratings are based on a maximum junction temperature of 150 °C.
- 2. These are steady-state limits. Fairchild Semiconductor should be consulted on applications involving pulsed or low-duty cycle operations.

Thermal Characteristics⁽³⁾

Values are at $T_A = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter	Max.	Unit
Р	Total Device Dissipation	300	mW
PD	Derate Above 25°C	2.4	mW/°C
R_{\thetaJA}	Thermal Resistance, Junction to Ambient	415	°C/W

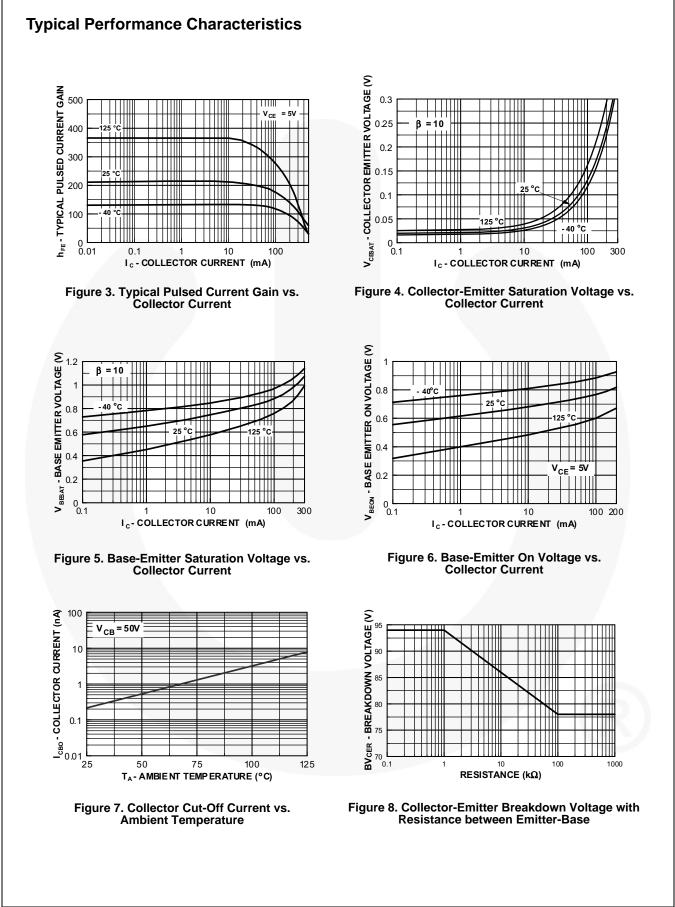
Note:

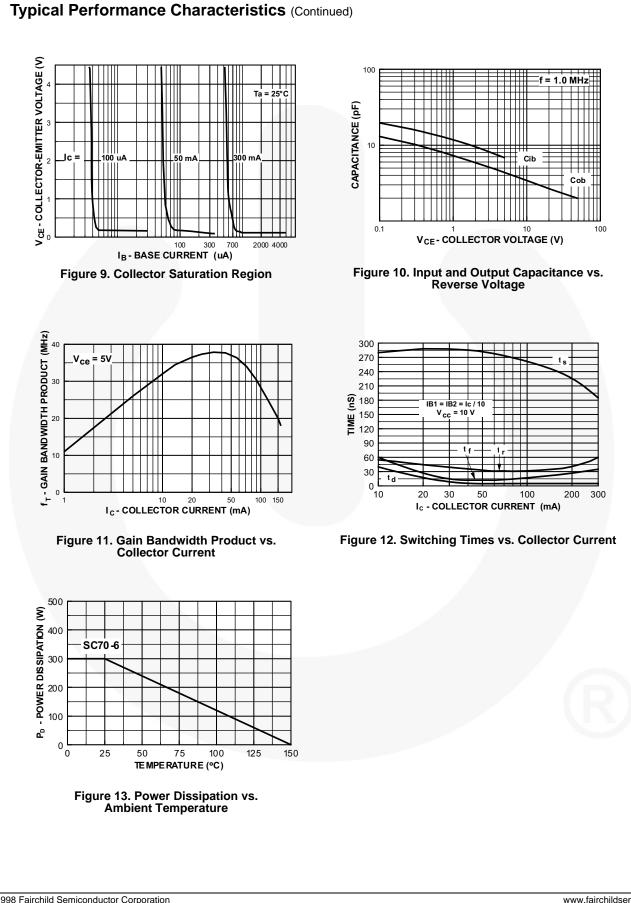
3. PCB size: FR-4 76 x 114 x 1.57 mm3 (3.0 inch x 4.5 inch x 0.062 inch) with minimum land pattern size.

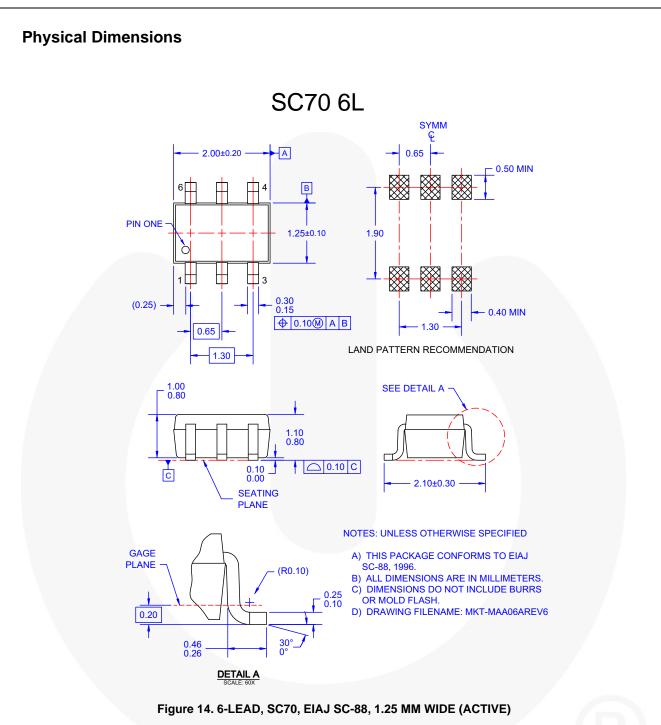
Electrical Characteristics

Values are at $T_A = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = -10 mA, I _B = 0	-45			V
V _{(BR)CES}	Collector-Base Breakdown Voltage	I _C = -10 μA, I _E = 0	-50			V
V _{(BR)CBO}	Collector-Base Breakdown Voltage	$I_{\rm C} = -10 \ \mu {\rm A}, \ I_{\rm E} = 0$	-50			V
V _{(BR)EBO}	Emitter-Base Breakdown Voltage	I _E = -10 μA, I _C = 0	-5			V
	Collector Cut-Off Current	V _{CB} = -30 V			-15	nA
I _{CBO}		$V_{CB} = -30$ V, $T_{A} = 150$ °C			-4	μA
h _{FE}	DC Current Gain	I_{C} = -2.0 mA, V_{CE} = -5.0 V	125		630	
V (act)	Collector-Emitter Saturation Voltage	I _C = -10 mA, I _B = -0.5 mA			-0.30	v
V _{CE} (sat)		I _C = -100 mA, I _B = -5.0 mA			-0.65	v
)/ (op)	Base-Emitter On Voltage	I_{C} = -2.0 mA, V_{CE} = -5.0 V	-0.60		-0.75	V
V _{BE} (on)		I _C = -10 mA, V _{CE} = -5.0 V			-0.82	v
f _T	Current Gain-Bandwidth Product	I _C = -10 mA, V _{CE} = -5.0, f = 100 MHz		200		MHz
C _{ob}	Output Capacitance	V _{CB} = -10 V, f = 1.0 MHz		3.5		pF
NF	Noise Figure	$\begin{split} I_{C} &= -0.2 \text{ mA}, \text{ V}_{CE} = -5.0 \text{ V}, \\ R_{S} &= 2.0 \text{ k}\Omega, \text{ f} = 1.0 \text{ kHz}, \\ \text{BW} &= 200 \text{ Hz} \end{split}$		2.5		dB







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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
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