

**April 2011** 

# FDT434P

# P-Channel 2.5V Specified PowerTrench® MOSFET

## **General Description**

This P-Channel 2.5V specified MOSFET is produced using Fairchild Semiconductor's advanced PowerTrench process that has been especially tailored to minimize the on-state resistance and yet maintain low gate charge for superior switching performance.

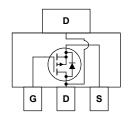
## **Applications**

- Low Dropout Regulator
- DC/DC converter
- · Load switch
- Motor driving

## **Features**

- -5.5 A, -20 V.  $R_{DS(ON)}$  = 0.050  $\Omega$  @  $V_{GS}$  = -4.5 V  $R_{DS(ON)}$  = 0.070  $\Omega$  @  $V_{GS}$  = -2.5 V.
- Low gate charge (13nC typical)
- High performance trench technology for extremely low  $R_{\mbox{\scriptsize DS}(\mbox{\scriptsize ON})}$  .
- High power and current handling capability in a widely used surface mount package.





## Absolute Maximum Ratings T<sub>A</sub>=25°C unless otherwise noted

Symbol	I Parameter		Ratings	Units
V <sub>DSS</sub>	Drain-Source Voltage		-20	V
V <sub>GSS</sub>	Gate-Source Voltage		±8	V
I <sub>D</sub>	Drain Current - Continuous	(Note 1a)	-6	Α
	- Pulsed		-30	
P <sub>D</sub>	Power Dissipation for Single Operation	(Note 1a)	3	W
		(Note 1b)	1.3	
		(Note 1c)	1.1	
T <sub>J</sub> , T <sub>stg</sub>	Operating and Storage Junction Temperature Range		-55 to +150	°C

## **Thermal Characteristics**

$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	(Note 1a)	42	°C/W
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	(Note 1)	12	°C/W

## **Package Marking and Ordering Information**

Device Marking	Device	Reel Size	Tape width	Quantity	
434	FDT434P	13"	12mm	2500 units	

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
Off Char	acteristics			ı	I.	I.
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	-20			V
<u>ΔBV<sub>DSS</sub></u> ΔT <sub>J</sub>	Breakdown Voltage Temperature Coefficient	$I_D$ = -250 $\mu$ A,Referenced to 25°C		-28		mV/°C
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	$V_{DS} = -16 \text{ V},  V_{GS} = 0 \text{ V}$			-1	μΑ
I <sub>GSSF</sub>	Gate–Body Leakage Current, Forward	V <sub>GS</sub> = 8 V, V <sub>DS</sub> = 0 V			100	nA
I <sub>GSSR</sub>	Gate–Body Leakage Current, Reverse	$V_{GS} = -8 \text{ V}$ $V_{DS} = 0 \text{ V}$			-100	nA
On Char	acteristics (Note 2)					
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	-0.4	-0.6	-1	V
$\Delta V_{GS(th)} = \Delta T_J$	Gate Threshold Voltage Temperature Coefficient	$I_D$ = -250 $\mu$ A,Referenced to 25°C		2		mV/°C
R <sub>DS(on)</sub>	Static Drain–Source On–Resistance	V <sub>GS</sub> = -4.5 V, I <sub>D</sub> = -6 A V <sub>GS</sub> = -2.5 V, I <sub>D</sub> = -4 A V <sub>GS</sub> = -4.5 V, I <sub>D</sub> = -6 A T <sub>J</sub> =125°C		0.040 0.050 0.067	0.050 0.070 0.083	Ω
I <sub>D(on)</sub>	On–State Drain Current	$V_{GS} = -4.5 \text{ V}, I_D = -6 \text{ A T}_J = 125^{\circ}\text{C}$ $V_{GS} = -4.5 \text{ V}, V_{DS} = -5 \text{ V}$	-20			Α
<b>g</b> FS	Forward Transconductance	$V_{DS} = -10 \text{ V}, \qquad I_{D} = -6 \text{ A}$		6.5		S
Dvnamio	Characteristics					
C <sub>iss</sub>	Input Capacitance	$V_{DS} = -10 \text{ V}, \qquad V_{GS} = 0 \text{ V},$		1187		pF
Coss	Output Capacitance	f = 1.0 MHz		270		pF
C <sub>rss</sub>	Reverse Transfer Capacitance	1		114		pF
Switchir	g Characteristics (Note 2)					
t <sub>d(on)</sub>	Turn-On Delay Time	$V_{DD} = -5 \text{ V}, \qquad I_{D} = -1 \text{ A},$		8	16	ns
t <sub>r</sub>	Turn-On Rise Time	$V_{GS} = -4.5 \text{ V},  R_{GEN} = 6 \Omega$		15	25	ns
t <sub>d(off)</sub>	Turn-Off Delay Time	1		45	65	ns
t <sub>f</sub>	Turn–Off Fall Time	7		30	50	ns
Q <sub>g</sub>	Total Gate Charge	$V_{DS} = -10 \text{ V}, \qquad I_{D} = -6 \text{ A},$		13	19	nC
Q <sub>gs</sub>	Gate-Source Charge	$V_{GS} = -4.5 \text{ V}$		1.8		nC
$Q_{gd}$	Gate-Drain Charge			3		nC
Drain-S	ource Diode Characteristics	and Maximum Ratings				
Is	Maximum Continuous Drain-Source				-2.5	Α
V <sub>SD</sub>	Drain–Source Diode Forward Voltage	$V_{GS} = 0 \text{ V},  I_S = -2.5 \text{ A}  \text{(Note 2)}$		-0.75	-1.2	V

## Notes

<sup>1.</sup>  $R_{\theta JA}$  is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins.  $R_{\theta JC}$  is guaranteed by design while  $R_{\theta CA}$  is determined by the user's board design.



a) 42°C/W when mounted on a 1in² pad of 2 oz copper



b) 95°/W when mounted on a .0066 in² pad of 2 oz copper

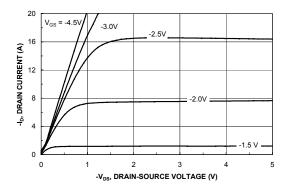


c) 110°/W when mounted on a minimum pad.

Scale 1 : 1 on letter size paper

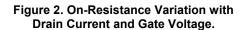
**2.** Pulse Test: Pulse Width <  $300\mu$ s, Duty Cycle < 2.0%

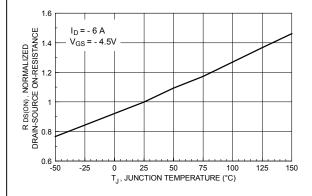
## **Typical Characteristics**



1.8 V<sub>GS</sub> = -2.5V V<sub>GS</sub> = -2.5V V<sub>GS</sub> = -2.5V V<sub>GS</sub> = -3.5V V<sub>GS</sub> = -3.5

Figure 1. On-Region Characteristics.





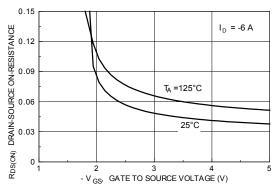
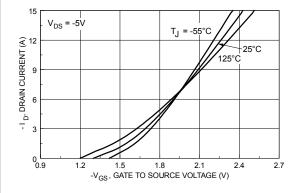


Figure 3. On-Resistance Variation withTemperature.

Figure 4. On-Resistance Variation with Gate-to-Source Voltage.



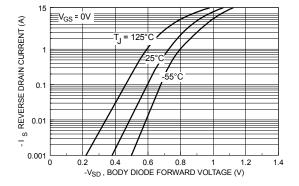
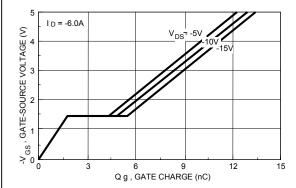


Figure 5. Transfer Characteristics.

Figure 6. Body Diode Forward Voltage Variation with Source Current and Temperature.

## **Typical Characteristics**



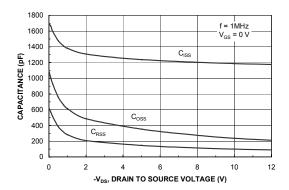
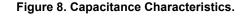
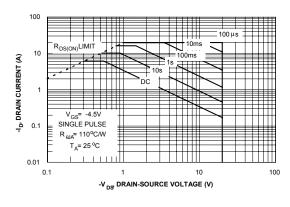


Figure 7. Gate Charge Characteristics.





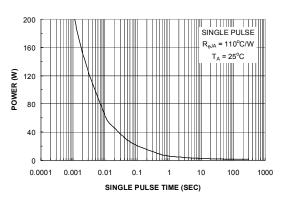


Figure 9. Maximum Safe Operating Area.

Figure 10. Single Pulse Maximum Power Dissipation.

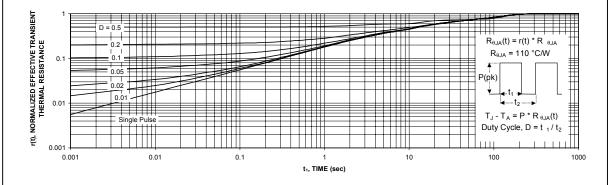


Figure 11. Transient Thermal Response Curve.

Thermal characterization performed using the conditions described in Note 1c. Transient thermal response will change depending on the circuit board design.





### TRADEMARKS

The following includes registered and unregistered trademarks and service marks, owned by Fairchild Semiconductor and/or its global subsidiaries, and is not intended to be an exhaustive list of all such trademarks.

AccuPower™ Auto-SPM™ AX-CAPTM\* Build it Now™ CorePLUS™ CorePOWER™

CROSSVOLT<sup>TM</sup> CTI TM Current Transfer Logic™ DEUXPEED® Dual Cool™ EcoSPARK® EfficentMax™ **ESBC™** 

F® Fairchild<sup>®</sup>

Fairchild Semiconductor® FACT Quiet Series™ FACT® FAST®

FastvCore™ FETBench™ FlashWriter® \* **FPSTM** 

F-PFS™ FRFET®

Global Power Resource<sup>SM</sup> Green FPS™

Green FPS™ e-Series™

 $Gmax^{TM}$  $\mathsf{GTO^{\mathsf{TM}}}$ IntelliMAX™ ISOPLANAR™ MegaBuck™ MIČROCOUPLER™

MicroFET™ MicroPak™ MicroPak2™ MillerDrive™ MotionMax™ Motion-SPM™ mWSaver™ OptiHiT™ OPTOLOGIC® OPTOPLANAR®

PDP SPM™

Power-SPM™ PowerTrench® PowerXS<sup>TM</sup>

Programmable Active Droop™ OFFT

QSTM

Quiet Series™ RapidConfigure<sup>™</sup>

Saving our world, 1mW/W/kW at a time™ SignalWise™

SmartMax™ SMART START<sup>TM</sup> SPM $^{\circledR}$ STEALTH™ SuperFET<sup>®</sup> SuperSOT™-3 . SuperSOT™-6 SuperSOT™-8 SupreMOS® SyncFET™ Sync-Lock™

SYSTEM ®\* GENERAL

The Power Franchise®

The Right Technology for Your Success™

wer franchise TinyBoost™ TinyBuck™ TinyCalc™ TinyLogic<sup>®</sup> TINYOPTO™ TinyPower™ TinyPWM™ TinyWire™ TriFault Detect™ TRUECURRENT®\* սSerDes™

UHC<sup>®</sup> Ultra FRFET™ UniFET™ VCX™ VisualMax™ XS™

\*Trademarks of System General Corporation, used under license by Fairchild Semiconductor.

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION, OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS. THESE SPECIFICATIONS DO NOT EXPAND THE TERMS OF FAIRCHILD'S WORLDWIDE TERMS AND CONDITIONS, SPECIFICALLY THE WARRANTY THEREIN, WHICH COVERS THESE PRODUCTS.

LIFE SUPPORT POLICY
FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION.

- Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body or (b) support or sustain life, and (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury of the user.
- A critical component in any component of a life support, device, or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or

## ANTI-COUNTERFEITING POLICY

Fairchild Semiconductor Corporation's Anti-Counterfeiting Policy. Fairchild's Anti-Counterfeiting Policy is also stated on our external website, www.Fairchildsemi.com, under Sales Support.
Counterfeiting of semiconductor parts is a growing problem in the industry. All manufactures of semiconductor products are experiencing counterfeiting of their

parts. Customers who inadvertently purchase counterfeit parts experience many problems such as loss of brand reputation, substandard performance, failed application, and increased cost of production and manufacturing delays. Fairchild is taking strong measures to protect ourselves and our customers from the proliferation of counterfeit parts. Fairchild strongly encourages customers to purchase Fairchild parts either directly from Fairchild or from Authorized Fairchild Distributors who are listed by country on our web page cited above. Products customers buy either from Fairchild directly or from Authorized Fairchild Distributors are genuine parts, have full traceability, meet Fairchild's quality standards for handing and storage and provide access to Fairchild's full range of up-to-date technical and product information. Fairchild and our Authorized Distributors will stand behind all warranties and will appropriately address and warranty issues that may arise. Fairchild will not provide any warranty coverage or other assistance for parts bought from Unauthorized Sources. Fairchild is committed to combat this global problem and encourage our customers to do their part in stopping this practice by buying direct or from authorized distributors.

## PRODUCT STATUS DEFINITIONS = 0.5 Definition of Terms

Datasheet Identification	Product Status	Definition
Advance Information	Formative / In Design	Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
Obsolete	Not In Production	Datasheet contains specifications on a product that is discontinued by Fairchild Semiconductor. The datasheet is for reference information only.

Rev. 153

# AMEYA360 Components Supply Platform

# **Authorized Distribution Brand:**

























## Website:

Welcome to visit www.ameya360.com

## Contact Us:

## > Address:

401 Building No.5, JiuGe Business Center, Lane 2301, Yishan Rd Minhang District, Shanghai , China

## > Sales:

Direct +86 (21) 6401-6692

Email amall@ameya360.com

QQ 800077892

Skype ameyasales1 ameyasales2

## Customer Service :

Email service@ameya360.com

# Partnership :

Tel +86 (21) 64016692-8333

Email mkt@ameya360.com