

October 2013

# FGB3236\_F085 / FGI3236\_F085

# EcoSPARK<sup>a</sup> 320mJ, 360V, N-Channel Ignition IGBT

## **Features**

- Industry Standard D<sup>2</sup>-Pak package
- SCIS Energy = 320mJ at T<sub>J</sub> = 25°C
- Logic Level Gate Drive
- Qualified to AEC Q101
- RoHS Compliant

# **Applications**

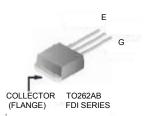
- Automotive Ignition Coil Driver Circuits
- Coil On Plug Applications



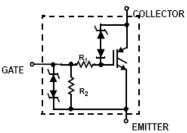
## Package



JEDEC TO-263AB D<sup>2</sup>-Pak



# **Symbol**



# **Device Maximum Ratings** $T_A = 25^{\circ}C$ unless otherwise noted

| Symbol              | Parameter                                                                                                     | Ratings     | Units |
|---------------------|---------------------------------------------------------------------------------------------------------------|-------------|-------|
| BV <sub>CER</sub>   | Collector to Emitter Breakdown Voltage (I <sub>C</sub> = 1mA)                                                 | 360         | V     |
| BV <sub>ECS</sub>   | Emitter to Collector Voltage - Reverse Battery Condition (I <sub>C</sub> = 10mA)                              | 24          | V     |
| E <sub>SCIS25</sub> | Self Clamping Inductive Switching Energy ( $I_{SCIS} = 14.7A, L = 3.0 \text{mHy}, T_J = 25^{\circ}\text{C}$ ) | 320         | mJ    |
|                     | Self Clamping Inductive Switching Energy ( $I_{SCIS} = 10.4A$ , L = 3.0mHy, $T_J = 150$ °C)                   | 160         | mJ    |
| I <sub>C25</sub>    | Collector Current Continuous, at V <sub>GE</sub> = 4.0V, T <sub>C</sub> = 25°C                                | 44          | Α     |
| I <sub>C110</sub>   | Collector Current Continuous, at V <sub>GE</sub> = 4.0V, T <sub>C</sub> = 110°C                               | 27          | Α     |
| $V_{GEM}$           | Gate to Emitter Voltage Continuous                                                                            | ±10         | V     |
| D                   | Power Dissipation Total, at T <sub>C</sub> = 25°C                                                             | 187         | W     |
| $P_{D}$             | Power Dissipation Derating, for T <sub>C</sub> > 25°C                                                         | 1.25        | W/°C  |
| T <sub>J</sub>      | Operating Junction Temperature Range                                                                          | -40 to +175 | °C    |
| T <sub>STG</sub>    | Storage Junction Temperature Range                                                                            | -40 to +175 | °C    |
| T <sub>L</sub>      | Max. Lead Temp. for Soldering (Leads at 1.6mm from case for 10s)                                              | 300         | °C    |
| T <sub>PKG</sub>    | Max. Lead Temp. for Soldering (Package Body for 10s)                                                          | 260         | °C    |
| ESD                 | Electrostatic Discharge Voltage at 100 pF, 1500 $\Omega$                                                      | 4           | kV    |

# Package Marking and Ordering Information

| Device Marking | Device       | Package | Reel Size | Tape Width | Quantity  |
|----------------|--------------|---------|-----------|------------|-----------|
| FGB3236        | FGB3236_F085 | TO263   | 330mm     | 24mm       | 800 units |
| FGI3236        | FGI3236_F085 | TO262   | Tube      | NA         | 50 units  |

# **Electrical Characteristics** $T_A = 25^{\circ}C$ unless otherwise noted

| Symbol Parameter T | est Conditions | Min | Тур | Max | Units |
|--------------------|----------------|-----|-----|-----|-------|
|--------------------|----------------|-----|-----|-----|-------|

#### **Off State Characteristics**

| BV <sub>CER</sub> | Collector to Emitter Breakdown Voltage | $I_{CE} = 2\text{mA}, V_{GE} = 0,$<br>$R_{GE} = 1\text{K}\Omega, \text{ See Fig. 15}$<br>$T_{J} = -40 \text{ to } 150^{\circ}\text{C}$ |                                  | 330 | 363 | 390 | <b>V</b> |
|-------------------|----------------------------------------|----------------------------------------------------------------------------------------------------------------------------------------|----------------------------------|-----|-----|-----|----------|
| BV <sub>CES</sub> | Collector to Emitter Breakdown Voltage | $T_{\rm J} = -40 \text{ to } 150^{\rm o}\text{C}$                                                                                      |                                  | 350 | 378 | 410 | <b>V</b> |
| BV <sub>ECS</sub> | Emitter to Collector Breakdown Voltage | $I_{CE} = -75 \text{mA}, V_{GE} = 0 \text{V},$ $T_{C} = 25 ^{\circ}\text{C}$                                                           |                                  | 30  | -   | -   | V        |
| $BV_{GES}$        | Gate to Emitter Breakdown Voltage      | I <sub>GES</sub> = ±2mA                                                                                                                |                                  | ±12 | ±14 | -   | V        |
| 1                 |                                        | V <sub>CES</sub> = 250V,                                                                                                               | $T_{\rm C} = 25^{\rm o}{\rm C}$  | 1   | 1   | 25  | μΑ       |
| ICES              | Collector to Emitter Leakage Current   | See Fig. 11                                                                                                                            | $T_{\rm C} = 150^{\rm o}{\rm C}$ | -   | -   | 1   | mA       |
|                   | Emitter to Collector Lookage Current   | V <sub>EC</sub> = 24V,                                                                                                                 | $T_{\rm C} = 25^{\rm o}{\rm C}$  | -   | -   | 1   | m۸       |
| I <sub>ECS</sub>  | Emitter to Collector Leakage Current   | See Fig.11                                                                                                                             | $T_{\rm C} = 150^{\rm o}{\rm C}$ | -   | -   | 40  | mA       |
| R <sub>1</sub>    | Series Gate Resistance                 |                                                                                                                                        |                                  | -   | 100 | -   | Ω        |
| R <sub>2</sub>    | Gate to Emitter Resistance             |                                                                                                                                        |                                  | 10K | -   | 30K | Ω        |

## **On State Characteristics**

| V <sub>CE(SAT)</sub> | Collector to Emitter Saturation Voltage | I <sub>CE</sub> = 6A, V <sub>GE</sub> = 4V, | T <sub>C</sub> =25°C,<br>See Fig. 3 | 1  | 1.14 | 1.4  | V |
|----------------------|-----------------------------------------|---------------------------------------------|-------------------------------------|----|------|------|---|
| V <sub>CE(SAT)</sub> | Collector to Emitter Saturation Voltage | $H_{\alpha -} = 100  V_{\alpha -} = 4.5V$   | $T_C = 150$ °C,<br>See Fig. 4       | 1  | 1.32 | 1.7  | ٧ |
| $V_{CE(SAT)}$        | Collector to Emitter Saturation Voltage | $I_{CE} = 15A, V_{GE} = 4.5V,$              | $T_{\rm C} = 150^{\rm o}{\rm C}$    | -  | 1.61 | 2.05 | V |
| I <sub>CE(ON)</sub>  | Collector to Emitter On State Current   | $V_{GE}$ = 5V, $V_{CE}$ = 5V                |                                     | 50 | -    | -    | Α |

Max Units

Min

# **Electrical Characteristics** $T_A = 25^{\circ}C$ unless otherwise noted

Parameter

| Dynamic Characteristics |                                   |                                                                                   |                                  |      |     |     |    |  |  |
|-------------------------|-----------------------------------|-----------------------------------------------------------------------------------|----------------------------------|------|-----|-----|----|--|--|
| Q <sub>G(ON)</sub>      | Gate Charge                       | I <sub>CE</sub> = 10A, V <sub>CE</sub> = 12V,<br>V <sub>GE</sub> = 5V, See Fig.14 |                                  | -    | 20  | -   | nC |  |  |
| \/ ·                    | Gate to Emitter Threshold Voltage | I <sub>CE</sub> = 1mA, V <sub>CE</sub> = V <sub>GE</sub>                          | $T_{\rm C} = 25^{\rm o}{\rm C}$  | 1.3  | 1.6 | 2.2 | V  |  |  |
| $V_{GE(TH)}$            | Gate to Emitter Threshold Voltage | See Fig. 10                                                                       | $T_{\rm C} = 150^{\rm o}{\rm C}$ | 0.75 | 1.1 | 1.8 | v  |  |  |
| $V_{GEP}$               | Gate to Emitter Plateau Voltage   | V <sub>CE</sub> = 12V, I <sub>CE</sub> = 10A                                      |                                  | -    | 2.6 | -   | V  |  |  |

**Test Conditions** 

# **Switching Characteristics**

Symbol

| $t_{d(ON)R}$         | Current Turn-On Delay Time-Resistive  | 02 . 2                                                                                                                          | - | 0.65 | 4   | μS |
|----------------------|---------------------------------------|---------------------------------------------------------------------------------------------------------------------------------|---|------|-----|----|
| t <sub>rR</sub>      | Current Rise Time-Resistive           | $V_{GE}$ = 5V, $R_G$ = 1K $\Omega$<br>$T_J$ = 25°C, See Fig.12                                                                  | ı | 1.7  | 7   | μS |
| t <sub>d(OFF)L</sub> | Current Turn-Off Delay Time-Inductive | $V_{CE}$ = 300V, L = 500 $\mu$ Hy,                                                                                              | ı | 5.4  | 15  | μS |
| $t_{fL}$             | Current Fall Time-Inductive           | $V_{GE} = 5V, R_G = 1K\Omega$<br>$T_J = 25^{\circ}C, See Fig.12$                                                                | ı | 1.64 | 15  | μS |
| SCIS                 | Self Clamped inductive Switching      | $T_J = 25^{\circ}\text{C}$ , L = 3.0mHy, $I_{CE} = 14.7\text{A}$ , $R_G = 1\text{K}\Omega$ , $V_{GE} = 5\text{V}$ , See Fig.1&2 | ı | ı    | 320 | mJ |

## **Thermal Characteristics**

| I | $R_{\theta JC}$ | Thermal Resistance Junction to Case | All Packages | - | - | 0.8 | °C/W |
|---|-----------------|-------------------------------------|--------------|---|---|-----|------|
|   | 000             |                                     | 3 3          |   |   |     |      |

## **Typical Performance Curves**

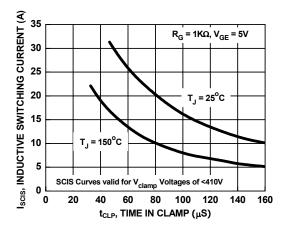


Figure 1. Self Clamped Inductive Switching Current vs. Time in Clamp

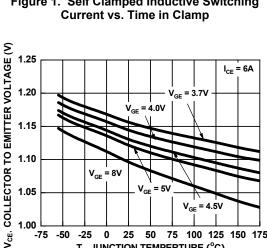


Figure 3. Collector to Emitter On-State Voltage vs. Junction Temperature

T,, JUNCTION TEMPERTURE (°C)

50 75 100 125 150 175

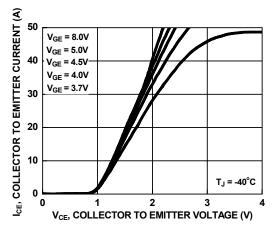


Figure 5. Collector to Emitter On-State Voltage vs. Collector Current

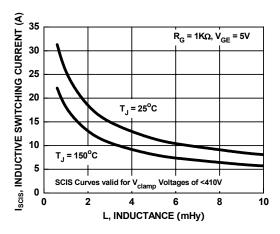


Figure 2. Self Clamped Inductive Switching Current vs. Inductance

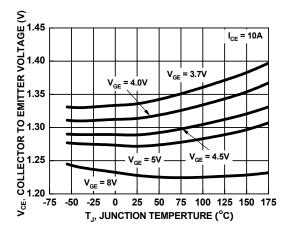


Figure 4. Collector to Emitter On-State Voltage vs. Junction Temperature

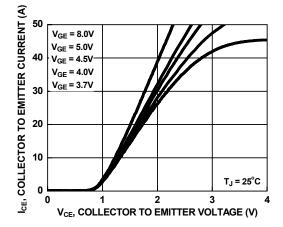


Figure 6. Collector to Emitter On-State Voltage vs. Collector Current

1.00

-75 -50 -25 0 25

## Typical Performance Curves (Continued)

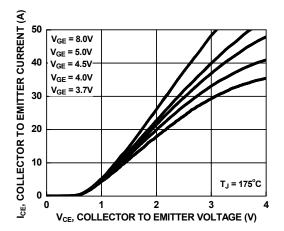


Figure 7. Collector to Emitter On-State Voltage vs. Collector Current

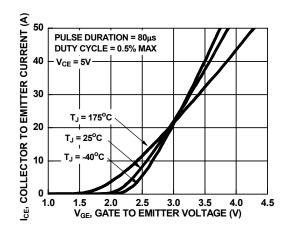


Figure 8. Transfer Characteristics

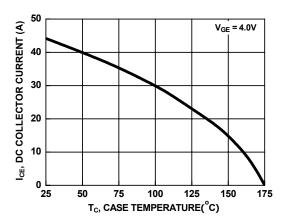


Figure 9. DC Collector Current vs. Case Temperature

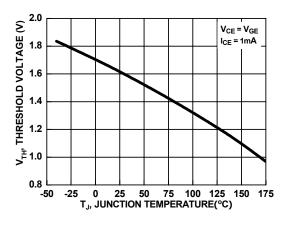


Figure 10. Threshold Voltage vs. Junction Temperature

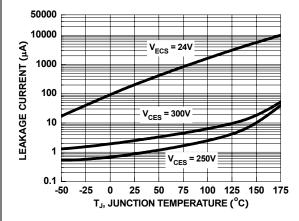


Figure 11. Leakage Current vs. Junction Temperature

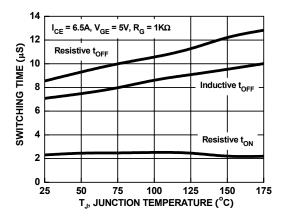
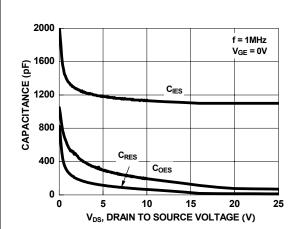


Figure 12. Switching Time vs. Junction Temperature



**Typical Performance Curves** (Continued)

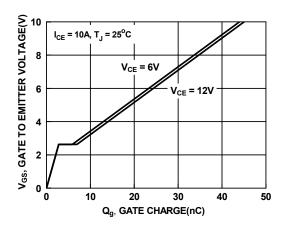


Figure 13. Capacitance vs. Collector to Emitter Voltage

Figure 14. Gate Charge

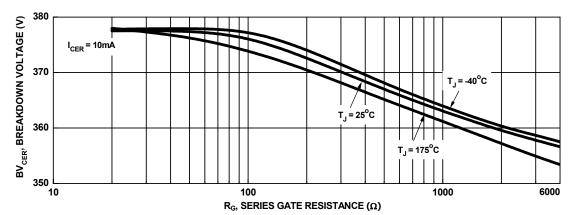


Figure 15. Break Down Voltage vs. Series Gate Resistance

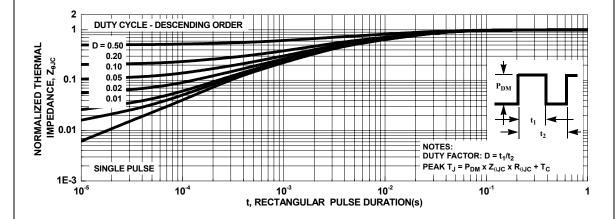


Figure 16. IGBT Normalized Transient Thermal Impedance, Junction to Case

# **Test Circuit and Waveforms**

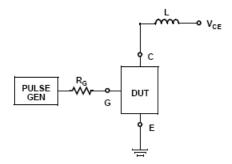


Figure 17. Inductive Switching Test Circuit

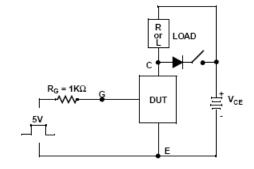


Figure 18.  $t_{ON}$  and  $t_{OFF}$  Switching Test Circuit

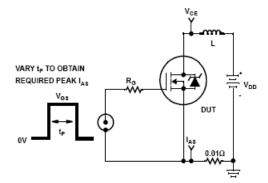


Figure 19. Energy Test Circuit

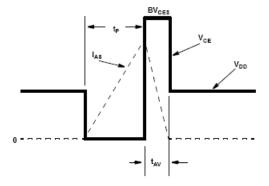
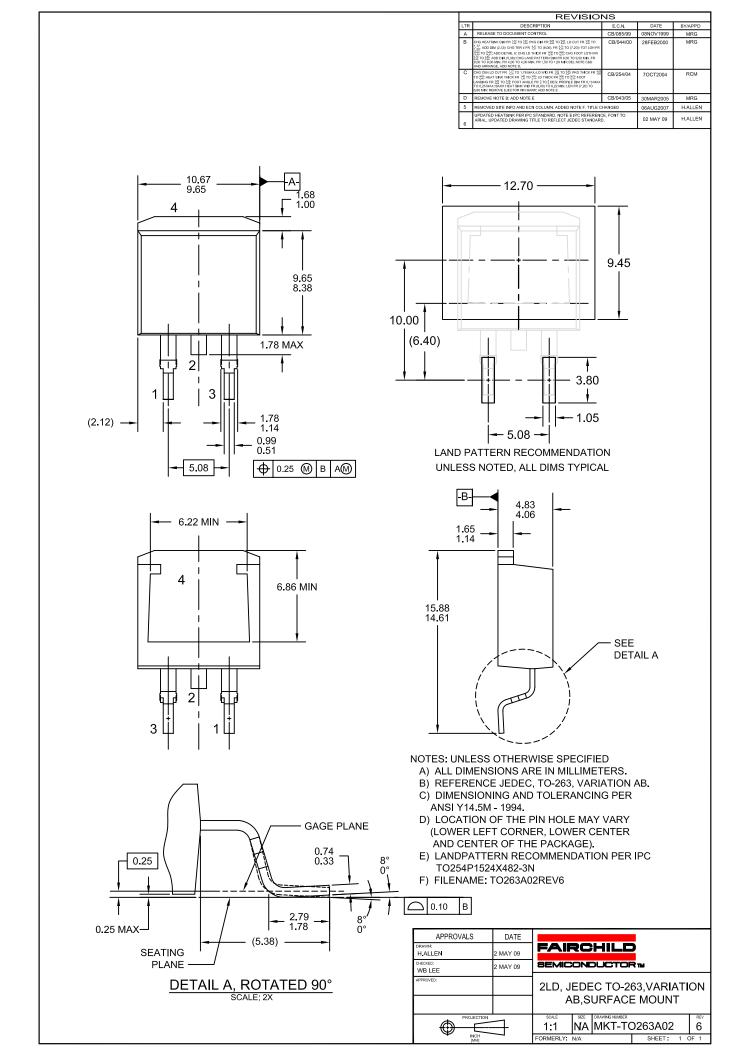
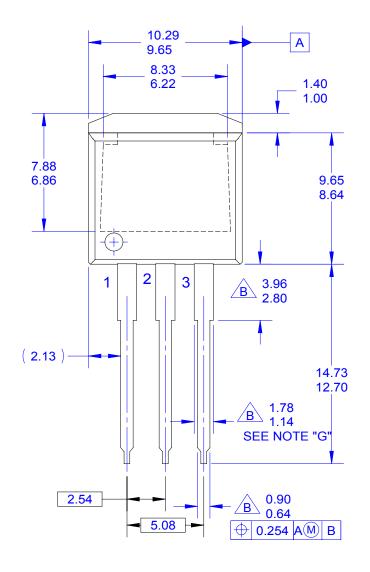
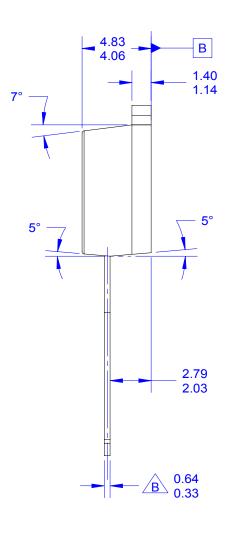


Figure 20. Energy Waveforms



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#### NOTES:

A. EXCEPT WHERE NOTED CONFORMS TO
TO262 JEDEC VARIATION AA.
B DOES NOT COMPLY JEDEC STD. VALUE.
C. ALL DIMENSIONS ARE IN MILLIMETERS.
D. DIMENSIONS ARE EXCLUSIVE OF BURRS,
MOLD FLASH AND TIE BAR PROTRUSIONS.
E. DIMENSION AND TOLERANCE AS PER ANSI
V14 5-1904

F. LOCATION OF PIN HOLE MAY VARY
(LOWER LEFT CORNER, LOWER CENTER
AND CENTER OF PACKAGE)
G. MAXIMUM WIDTH FOR F102 DEVICE = 1.35 MAX.

H. DRAWING FILE NAME: TO262A03REV5

| APPROVALS                 | DATE      |                                                |          |       |          |          |
|---------------------------|-----------|------------------------------------------------|----------|-------|----------|----------|
| BOBOY MALDO               | 11FEB2010 | F/                                             | 4IR      | CHILE | _        |          |
| CHECKED: KH LEE           |           | SEMICONDUCTOR 1M                               |          |       |          |          |
| APPROVED: BY HUANG        |           | TO262 3LD JEDEC                                |          |       |          |          |
| APPROVED:<br>HOWARD ALLEN |           | VARIATION AA                                   |          |       |          |          |
| PROJECTION [MM]   INCH    |           | SCALE SIZE DRAWING NUMBER 1:1 N/A MKT-TO262A03 |          |       |          | FEV<br>5 |
|                           |           | FORMER                                         | RLY: N/A |       | SHEET: 1 | OF 1     |





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|--------------------------|-----------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| Datasheet Identification |                       | Definition                                                                                                                                                                                          |
| Advance Information      | Formative / In Design | Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.                                                                       |
| Preliminary              | First Production      | Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design. |
| No Identification Needed | Full Production       | Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.                                               |
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# AMEYA360 Components Supply Platform

# **Authorized Distribution Brand:**

























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