

bq26200EVM-001

Single Cell Battery Monitor Evaluation Module

User's Guide

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EVM WARNINGS AND RESTRICTIONS

It is important to operate this EVM within the input voltage range of 2.8 V–5.5 V and the output voltage range of 2.8 V–5.5 V.

Exceeding the specified input range may cause unexpected operation and/or irreversible damage to the EVM. If there are questions concerning the input range, please contact a TI field representative prior to connecting the input power.

Applying loads outside of the specified output range may result in unintended operation and/or possible permanent damage to the EVM. Please consult the EVM User's Guide prior to connecting any load to the EVM output. If there is uncertainty as to the load specification, please contact a TI field representative.

During normal operation, some circuit components may have case temperatures greater than 70°C. The EVM is designed to operate properly with certain components above 70°C as long as the input and output ranges are maintained. These components include but are not limited to linear regulators, switching transistors, pass transistors, and current sense resistors. These types of devices can be identified using the EVM schematic located in the EVM User's Guide. When placing measurement probes near these devices during operation, please be aware that these devices may be very warm to the touch.

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Post Office Box 655303
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bq26200 Battery Monitor Evaluation Module

The bq26200EVM-001 is a complete evaluation system for the bq26200 flash-based battery monitoring IC. The EVM includes one bq26200 circuit module, an EV2200 PC interface board for gas-gauge evaluation, a PC serial cable, and Windows-based PC software. The circuit module includes one bq26200 and all other components on-board necessary to monitor capacity and other critical parameters in a one-cell Li-Ion battery pack. The circuit module connects directly across the cell in a battery. With the EV2200 interface board and software, the user can read the bq26200 data registers and evaluate the functions of the bq26200 under different charge and discharge conditions.

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1.1 Features

- ☐ Complete evaluation system for the bq26200 battery monitor IC
- ☐ Populated circuit module for quick setup
- ☐ PC software and interface board for easy evaluation
- ☐ Software allows data logging for system analysis

1.2 Kit Contents

- ☐ 1 bq26200 circuit module with sense resistor
- ☐ 1 EV2200 PC interface board
- ☐ 1 Set of evaluation software disks entitled EV2200–26200
- ☐ 1 PC serial cable
- ☐ 1 Set of support documentation

1.3 Ordering Information

Table 1–1. Ordering Information

| EVM Part Number | Additional ICs | Chemistry | Pack Voltage | Capacity |
|-----------------|----------------|------------------|--------------|----------|
| bq26200EVM–001 | None | Li-Ion/NiMH/NiCd | 2.8 V–5.5 V | Any |

Circuit Module

The circuit module in the bq26200EVM-001 is a complete and compact example solution for battery monitoring of a single-cell Li-on pack. The circuit module incorporates a bq26200 battery monitor IC and all other components necessary to accurately protect and monitor the charge and discharge of one Li-Ion cell. This EVM can also be used for 3-series NiMH applications.

Contacts on the circuit module provide direct connection to the cell (BAT+, BAT–) and the serial communications port (HDQ). The system load and charger connect across PACK+ and PACK–.

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2.1 Pin Descriptions

| | |
|-------------------------------------|-----------------------------|
| <input type="checkbox"/> BAT+/PACK+ | Cell positive/pack positive |
| <input type="checkbox"/> BAT– | Cell negative |
| <input type="checkbox"/> PACK– | Pack negative |
| <input type="checkbox"/> HDQ | Serial communications port |
| <input type="checkbox"/> STAT | Status output |

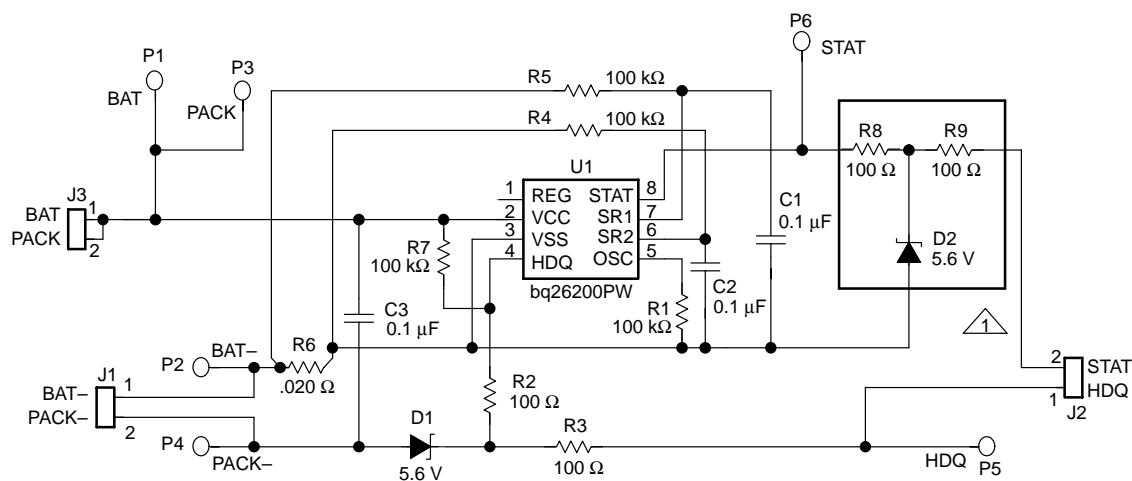
2.2 Circuit Module Schematic

The schematic shows the circuit for the bq26200EVM-001 implementation.

2.2.1 Schematic

Figure 2–1 is the bq26200EVM-001 (SLUP154) circuit module schematic diagram.

Figure 2–1. bq26200EVM-001 Schematic



Note: R8, R9, and D2 are required for external use only.

2.3 Circuit Module Physical Layouts

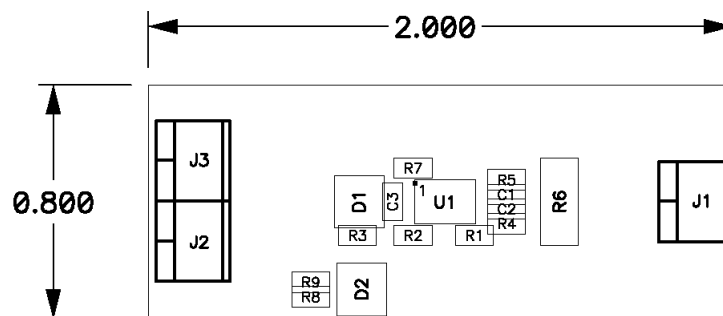
This section contains the board layout and assembly drawings for the bq26200EVM-001 circuit module (SLUP154).

2.3.1 Board Layout

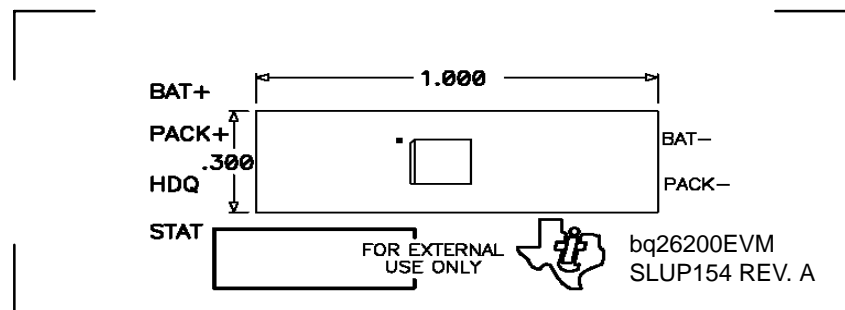
Figure 2–2 shows the PCB layers and assembly drawing for the circuit module.

Figure 2–2. bq26200EVM-001 Circuit Module Layouts

TOP ASSEMBLY



SILK 1



LAYER 1

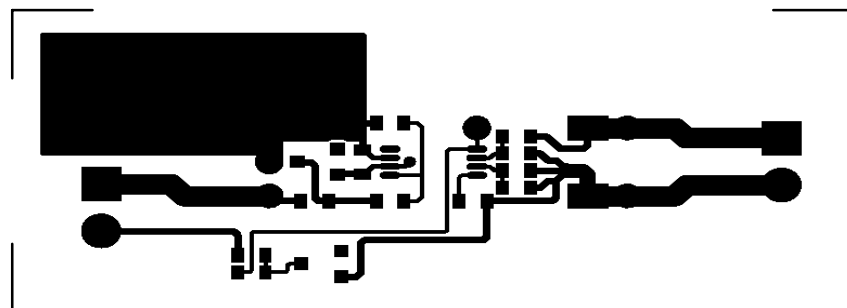


Figure 2–1. bq26200EVM-001 Circuit Module Layouts (Continued)



2.4 Bill of Materials

Table 2–1 lists materials required for the bq26200EVM–001 circuit module.

Table 2–1. Bill of Materials

| Ref Des | Qty-001 | Description | MFG | MFG Part# | Size |
|-----------------|---------|---|-----------|------------------|-------------|
| PCB | 1 | Printed-circuit board, 0.031, FR4, SMOBC, HASL | Any | | See FAB DWG |
| Terminal | 2 | Terminal block 3.5 mm | On Shore | ED555/2DS | 3.5 mm × 2 |
| C1, C2, C3 | 3 | Capacitor, ceramic, 0.1 μ F, 16 V, \pm 10%, X7R | Panasonic | ECJ-1VB1C104K | 603 |
| R6 | 1 | Resistor, 0.020 Ω , 1/2 W, 1% | ORC | LR2010-01-R020-F | 2010 |
| R1, R4, R5, R7 | 4 | Resistor, 100 k Ω , 1/16 W, 5% | Panasonic | ERJ-3GSYJ104 | 603 |
| R2, R3, R8, F9 | 4 | Resistor, 100 Ω , 1/16 W, 5% | Venkel | ERJ-3GSYJ101 | 603 |
| D1, D2 | 2 | Diode, zener, 5.6 V | Zetex | BZX84C5V6 | SPT-23 |
| U1 | 1 | Integrated circuit, gas gauge, bq26200PW | TI | bq26200PW | TSSOP-8 |
| Component count | 18 | | | | |

Assembly Notes:

1. This assembly is ESD sensitive.
2. This assembly must comply with IPC–A–610 class 2 or better.
3. This assembly must be clean of flux residues and contaminants.
4. Use of no-clean flux is not acceptable.

2.5 Performance Specification Summary

This section summarizes the performance specifications of the bq26200EVM-001 circuit module. Table 2–2 gives the performance specifications of the circuit.

Table 2–2. Performance Specification Summary

| Specification | MIN | TYP | MAX | UNIT |
|------------------------------|-----|-----|-----|------|
| Input voltage BAT+/BAT– | 2.8 | | 5.5 | V |
| Charge and discharge current | | | 4 | A |

Note: Maximum charge and discharge current is limited by the PCB design and power dissipation of the 20-mΩ, 1/2 W sense resistor included on the PCB.

EVM Hardware and Software Setup

This section describes how to install the EV2200-26200 PC software and how to connect the components of the EVM.

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3.1 Software Installation

The following steps install the EV2200-26200 software:

- 1) Insert disk 1 into a 3 1/2 inch floppy drive.
- 2) Select the 3 1/2 inch drive using **My Computer**.
- 3) Double-click on the **Setup.exe** icon.
- 4) The setup program prompts for the remaining disks and installs a Windows application group.

3.2 Hardware Installation

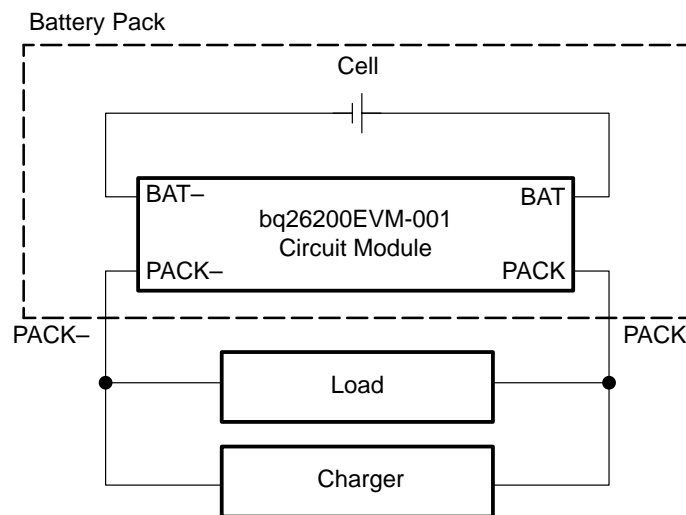
There are three hardware components to the bq26200EVM-001:

- 1) The bq26200EVM-001 circuit module
- 2) The EV2200 PC interface board
- 3) The PC

3.2.1 Connecting the bq26200EVM-001 Circuit Module to a Battery Pack

Figure 3–1 shows how to connect the bq26200EVM-001 circuit module to a Li-Ion cell and the system load/charger.

Figure 3–1. bq26200EVM-001 Circuit Module Connection to Cells and System Load/Charger



3.2.2 PC Interface Connection

The following steps configure the hardware for interface to the PC:

- 1) Connect bq26200EVM-001 to the EV2200 using wire leads as in the following table.

Table 3–1. Circuit Module-to-EV2200 Connection

| bq26200 Based Battery | EV2200 |
|------------------------------|---------------|
| HDQ | SMBD/HDQ1 |
| PACK– | VSS |

- 2) Connect the PC serial cable to the EV2200 and the PC COM port.

The bq26200EVM-001 is now set up for operation.

Operation

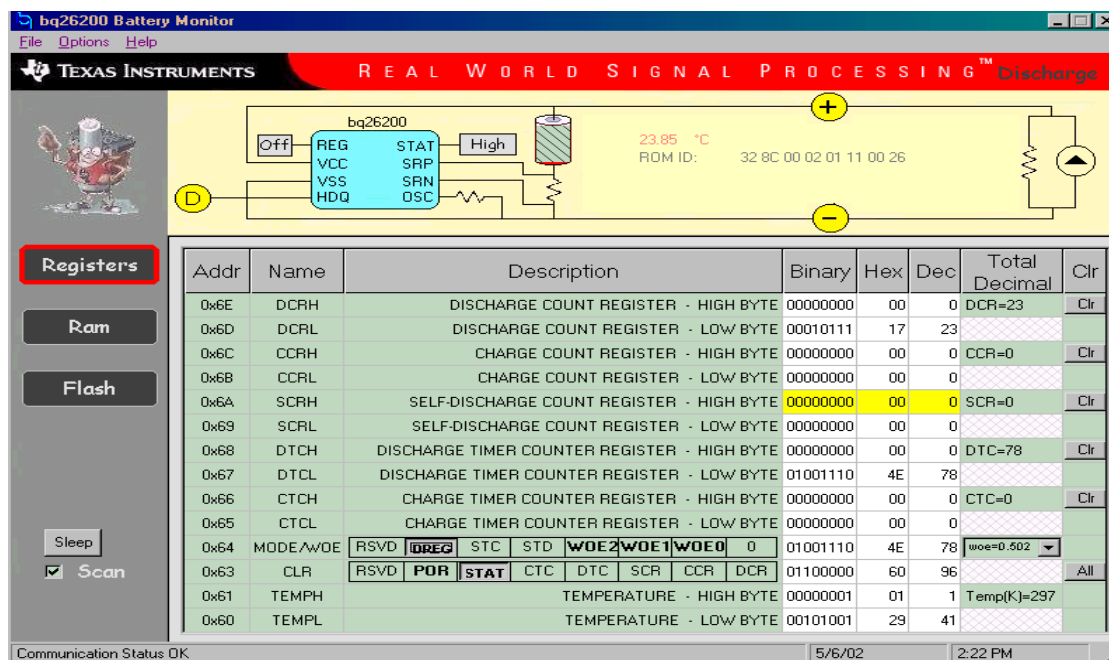
This chapter details the operation of the bq26200EVM-001 software.

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4.1 Starting the Program

Run EV2200–26200 from the **Start|Programs|Texas Instruments|bq26200 Battery Monitor** menu sequence. The software defaults to PC Com port 1 for communication. If the EV2200 is connected to Com port 1, the program should load and display the initial data screen.

Figure 4–1. Initial Register Screen



If the EV2200 is connected to PC Com port 2 or no port, the program displays a blank screen in Binary, Hex, Dec, and Total Decimal columns.

Once the appropriate Com Port is selected, the program loads the initial register screen. The com port can be changed by selecting the port under the Options menu.

4.2 Initialization

This section describes the settings required before the bq26200 is evaluated.

4.2.1 Setting Programmable bq26200 Options

4.2.1.1 Wake-Up Threshold

Select the wake-up threshold with the **woe** pulldown tab at location 0x64. This designates the potential across the sense resistor, below which the bq26200 goes to sleep.

4.2.1.2 DREG Box

If no regulator is used, DREG bit at 0x64 location can be set by clicking the DREG box to reduce the device supply current. To reset, click the box again.

4.2.1.3 STAT Box

To turn off the STAT output, set the STAT pin by clicking STAT box at 0x63 location. To reset, click the box again.

4.2.2 Register Clearing

Each counter in the bq26200 can be cleared (set to 00) by depressing the adjacent **Clr** button. The **All** button clears all counting registers in one sequence.

4.3 Reading and Writing Flash Memory

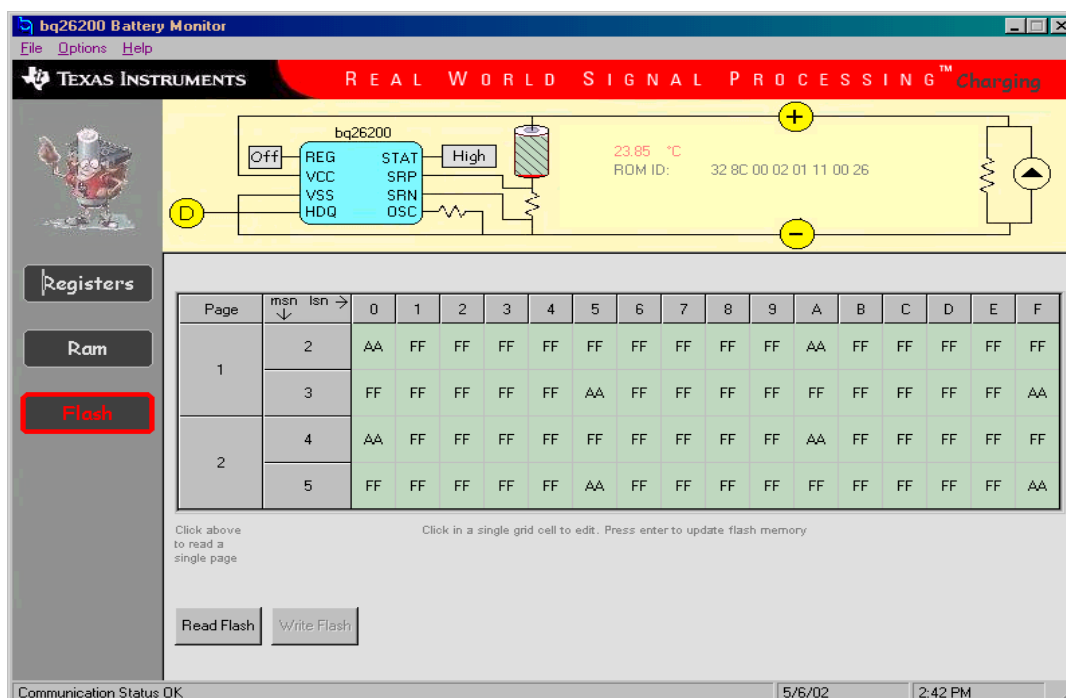
The flash memory on the bq26200 can store battery-pack specific parameters that can be used by the host system in its capacity monitoring algorithm.

To read the flash memory, click on the **Flash** button to select the flash data screen. Click the **Read Flash** button to read all flash pages or click on the individual page number to read the pages individually.

Each flash location can be modified by clicking on the individual location and entering the new value in hex.

The flash values can be stored to disk as one data file. To do this, select **File|Save Flash File** and enter the file name in the **Store Flash File** window. The file can subsequently be opened using the **File|Open Flash File** command from the pull down menu. Once the flash file is loaded into the program, the **Write Flash** button can be used to store the data in the bq26200.

Figure 4–2. Flash Register Screen



4.4 RAM Addressing

The RAM page of memory can be viewed by selecting the **RAM** data screen.

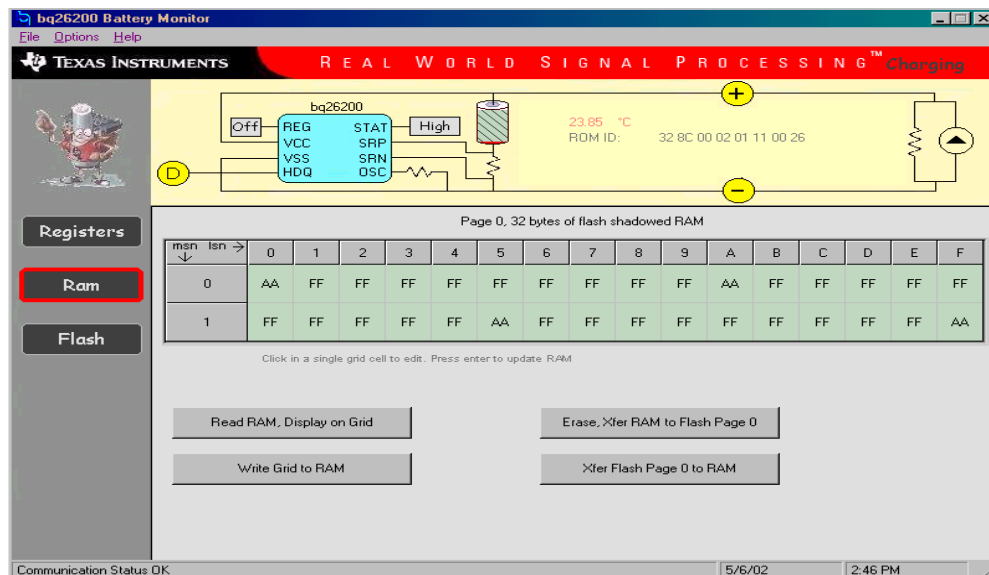
Click on the **Read RAM** button to read the RAM locations.

Each RAM location can be modified by clicking on the individual location and entering the new value in hex.

The RAM values can be stored to disk as one data file. To do this, select **File|Save RAM File** and enter the file name in the **Store Ram File** window. The file can subsequently be opened using the **File|Open Ram File** command from the pulldown menu. Once the FLASH file is loaded into the program the **Write RAM** button can be used to store the data in the bq26200.

On this screen, two other buttons are available to transfer data RAM to flash page 0 and from flash page 0 to RAM.

Figure 4–3. RAM Register Screen



4.5 Data Logging

The bq26200 registers can be logged by using the data log function. To log the data and create a log file

- 1) Select **Options|Logging Options** to select the registers to log and set the logging interval.

Figure 4–4. Logging Screen

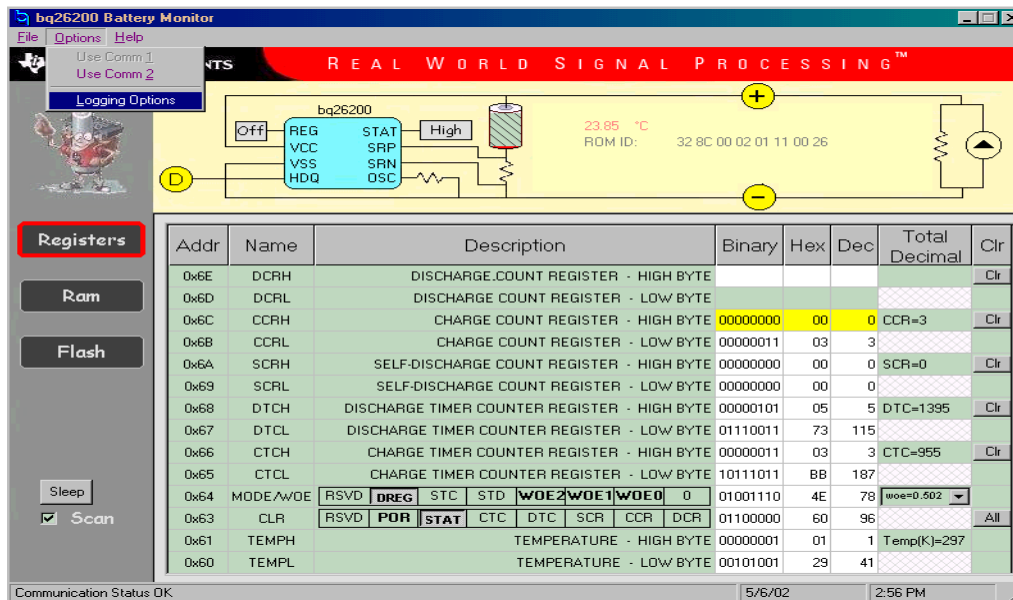
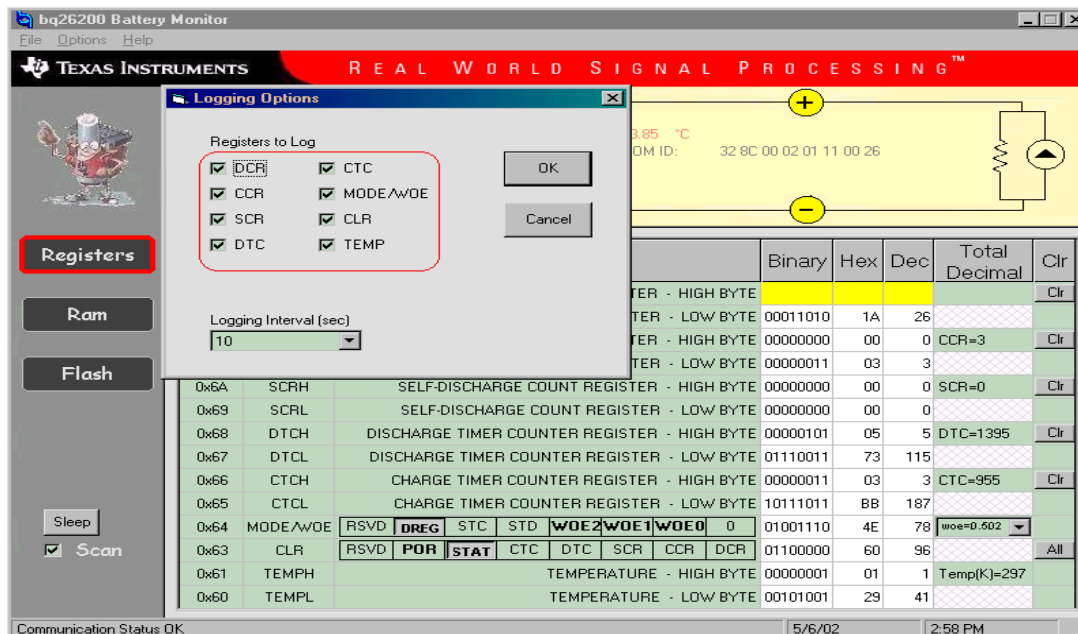


Figure 4–5. Logging Option Screen



- 2) **File|Start Data Log** from the pulldown menu.
- 3) Enter the name of the data log file in the **Name Datalog File** box
- 4) Click on the **Open** button
- 5) To stop the data logging, select **File|Close Data Log** from the pulldown menu

The file can be imported into a text editor, spreadsheet, or word processor program.

Example data log:

EV2200–26200 Version: 0.0.1

Texas Instruments bq26200 battery monitor

5/6/02 3:04:23 PM

ROM ID: 32 8C 00 02 01 11 00 26

| Sample | Stamp | Elapsed(s) | DCR | CCR | SCR | DTC | CTC | MODE/WOE | CLR | TEMP |
|--------|------------|------------|-----|-----|-----|------|-----|----------|-----|------|
| 1 | 3:04:33 PM | 10 | 27 | 3 | 0 | 2008 | 955 | 78 | 96 | 297 |
| 2 | 3:04:43 PM | 20 | 27 | 3 | 0 | 2008 | 955 | 78 | 96 | 297 |
| 3 | 3:04:53 PM | 30 | 27 | 3 | 0 | 2008 | 955 | 78 | 96 | 297 |
| 4 | 3:05:03 PM | 40 | 27 | 3 | 0 | 2008 | 955 | 78 | 96 | 297 |
| 5 | 3:05:13 PM | 50 | 27 | 3 | 0 | 2053 | 955 | 78 | 96 | 297 |
| 6 | 3:05:23 PM | 60 | 27 | 3 | 0 | 2063 | 955 | 78 | 96 | 297 |
| 7 | 3:05:33 PM | 70 | 27 | 3 | 0 | 2074 | 955 | 78 | 96 | 297 |
| 8 | 3:05:43 PM | 80 | 27 | 3 | 0 | 2086 | 955 | 78 | 96 | 297 |
| 9 | 3:05:53 PM | 90 | 27 | 3 | 0 | 2098 | 955 | 78 | 96 | 297 |
| 10 | 3:06:03 PM | 100 | 27 | 3 | 0 | 21–7 | 955 | 78 | 96 | 297 |
| 11 | 3:06:13 PM | 110 | 27 | 3 | 0 | 2123 | 955 | 78 | 96 | 297 |
| 12 | 3:06:23 PM | 120 | 27 | 3 | 0 | 2134 | 955 | 78 | 96 | 297 |

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401 Building No.5, JiuGe Business Center, Lane 2301, Yishan Rd
Minhang District, Shanghai , China

➤ Sales :

Direct +86 (21) 6401-6692

Email amall@ameya360.com

QQ 800077892

Skype ameyasales1 ameyasales2

➤ Customer Service :

Email service@ameya360.com

➤ Partnership :

Tel +86 (21) 64016692-8333

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