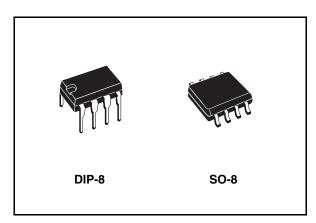


## Low power RS-485/RS-422 transceiver

#### **Features**

- Low quiescent current: 300 µA
- Designed for RS-485 interface application
- -7 V to 12 V common mode input voltage range
- Driver maintains high impedance in 3-state or with the power OFF
- 70 mV typical input hysteresis
- 30 ns propagation delay, 5 ns skew
- Operate from a single 5 V supply
- Current limiting and thermal shutdown for driver overload protection
- Allows up to 64 transceivers on the bus



The ST485 is available in three temperature range: commercial (0 °C to 70 °C), industrial (- 40 °C to 85 °C) and automotive (- 55 °C to 125 °C).

## **Description**

The ST485 is allow power transceiver for RS-485 and RS-422 communication. Each part contains one driver and one receiver.

This transceiver draw 300 µA (typ.) of supply current when unloaded or fully loaded with disabled drivers.

It operates from a single 5 V supply.

Driver is short-circuit current limited and is protected against excessive power dissipation by thermal shutdown circuitry that placed the driver outputs into a high-impedance state.

The ST485 is designed for bi-directional data communications on multipoint bus transmission line (half-duplex applications).

Table 1. Device summary

Order code	Temperature range	Package	Packaging
ST485CN	0 to 70 °C	DIP-8	50 parts per tube / 40 tube per box
ST485BN	- 40 to 85 °C	DIP-8	50 parts per tube / 40 tube per box
ST485CDR	0 to 70 °C	SO-8 (tape and reel)	2500 parts per reel
ST485BDR	- 40 to 85 °C	SO-8 (tape and reel)	2500 parts per reel

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Contents ST485B - ST485C

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ST485B - ST485C Pin configuration

# 1 Pin configuration

Figure 1. Pin connections

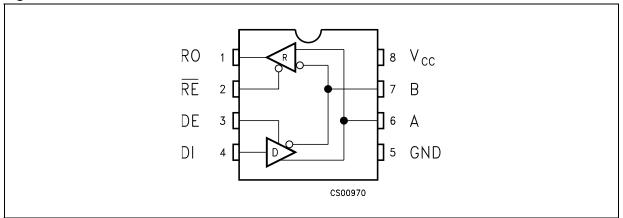


Table 2. Pin description

Pin n°	Symbol	Name and function
1	RO	Receiver output
2	RE	Receiver output enable
3	DE	Driver output enable
4	DI	Driver input
5	GND	Ground
6	A	Non-inverting receiver input and non-inverting driver output
7	В	Inverting receiver input and inverting driver output
8	V <sub>CC</sub>	Supply voltage

Truth tables ST485B - ST485C

## 2 Truth tables

Table 3. Truth table (driver)

Inputs			Out	outs
RE	DE	DI	В	Α
Х	Н	Н	L	Н
Х	Н	L	Н	L
Х	L	Х	Z	Z

Note:  $X = Don't \ care; Z = High \ impedance$ 

Table 4. Truth table (receiver)

Inputs			Outputs
RE	DE	A-B	RO
L	L	≥ +0.2V	Н
L	L	≤ <b>-</b> 0.2V	L
L	L	Inputs open	Н
Н	L	Х	Z

Note:  $X = Don't \ care; Z = High \ impedance$ 

ST485B - ST485C Maximum ratings

# 3 Maximum ratings

Table 5. Absolute maximum ratings

Symbol	Parameter	Value	Unit
V <sub>CC</sub>	Supply voltage	7	٧
VI	Control input voltage (RE, DE)	-0.5 to (V <sub>CC</sub> + 0.5)	٧
V <sub>DI</sub>	Driver input voltage (DI)	-0.5 to (V <sub>CC</sub> + 0.5)	٧
$V_{DO}$	Driver output voltage (A, B)	± 14	٧
V <sub>RI</sub>	Receiver input voltage (A, B)	± 14	٧
V <sub>RO</sub>	Receiver output voltage (RO)	-0.5 to (V <sub>CC</sub> + 0.5)	٧

Note: Absolute maximum ratings are those values beyond which damage to the device may occur. Functional operation under these condition is not implied.

Electrical characteristics ST485B - ST485C

# 4 Electrical characteristics

 $V_{CC}$  = 5 V ± 5 %,  $T_A$  =  $T_{MIN}$  to  $T_{MAX},$  unless otherwise specified. Typical values are referred to  $T_A$  = 25  $^{\circ}C$ 

Table 6. DC electrical characteristics

				Value				
Symbol	Parameter	Test conditions <sup>(1)</sup>	-4	0 to 85	°C	-55 to 125 °C		Unit
			Min.	Тур.	Max.	Min.	Max.	
V <sub>OD1</sub>	Differential driver output (no load)				5		5	V
V <sub>OD2</sub>	Differential driver output (with load)	$R_L = 27\Omega$ (RS-485) Figure 2 $R_L = 50\Omega$ (RS-422) Figure 2	1.5		5 5	1.4	5 5	V V
ΔV <sub>OD</sub>	Change in magnitude of driver differential output voltage for complementary output states	$R_L = 27\Omega$ or $50\Omega$ <i>Figure 2</i>			0.2		0.2	٧
V <sub>OC</sub>	Driver common-mode output voltage	$R_L = 27\Omega$ or $50\Omega$ <i>Figure 2</i>			3		3	V
ΔV <sub>OC</sub>	Change in magnitude of driver common-mode output voltage for complementary output states	$R_L = 27\Omega$ or $50\Omega$ <i>Figure 2</i>			0.2		0.2	٧
V <sub>IH</sub>	Input high voltage	RE, DE, DI	2.0			2.0		V
V <sub>IL</sub>	Input low voltage	RE, DE, DI			0.8		0.8	V
I <sub>IN1</sub>	Input current	RE, DE, DI			±2		± 2	μΑ
I <sub>IN2</sub>	Input current (A, B)	$V_{CM} = 0V \text{ or } 5.25V, V_{DE} = 0V$ $V_{IN} = 12V$ $V_{IN} = -7V$			1 -0.8		1 -0.8	mA mA
V <sub>TH</sub>	Receiver differential threshold voltage	V <sub>CM</sub> = -7 to 12V	-0.2		0.2	-0.2	0.2	V
$\Delta V_{TH}$	Receiver input hysteresis	V <sub>CM</sub> = 0V		70				mV
V <sub>OH</sub>	Receiver output high voltage	I <sub>O</sub> = -4mA, V <sub>ID</sub> = 200mV	3.5			3.4		V
V <sub>OL</sub>	Receiver output low voltage	I <sub>O</sub> = 4mA, V <sub>ID</sub> = -200mV			0.4		0.55	V
I <sub>OZR</sub>	3-state (high impedance) output current at receiver	V <sub>O</sub> = 0.4 to 2.4V			± 1		± 1	μΑ
R <sub>IN</sub>	Receiver input resistance	V <sub>CM</sub> = -7 to 12V	24			24		kΩ
I <sub>CC</sub>	No load supply current <sup>(2)</sup>	$V_{RE} = 0V \text{ or } V_{CC}$ $V_{DE} = V_{CC}$ $V_{DE} = 0V$		400 300	900 500		900 500	μ <b>Α</b> μ <b>Α</b>

Value **Symbol Parameter** Test conditions (1) -40 to 85 °C -55 to 125 °C Unit Max. Min. Тур. Min. Max. Driver short-circuit  $V_O = -7 \text{ to } 12V^{(3)}$ 250 35 250 35 mA I<sub>OSD1</sub> current, V<sub>O</sub>=High Driver short-circuit  $V_{O} = -7 \text{ to } 12V^{(3)}$ 35 250 35 250 mΑ I<sub>OSD2</sub> current, Vo=Low Receiver short-circuit  $V_O = 0V$  to  $V_{CC}$ 7 7 95 95 mΑ Iosr current

Table 6. DC electrical characteristics (continued)

 $V_{CC}$  = 5 V  $\pm$  5 %,  $T_A$  =  $T_{MIN}$  to  $T_{MAX}$ , unless otherwise specified. Typical values are referred to  $T_A$  = 25 °C

Table 7. Driver switching characteristics

				Value				
Symbol	Parameter	Test conditions <sup>(1)</sup>	-40 to 85 °C			-55 to 125°C		Unit
			Min.	Тур.	Max.	Min.	Max.	
t <sub>PLH</sub> t <sub>PHL</sub>	Propagation delay input to output	$R_{DIFF} = 54\Omega$ , $C_{L1} = C_{L2} = 100pF$ (See <i>Figure 4</i> and <i>Figure 6</i> )	10	30	60		70	ns
t <sub>SK</sub>	Output skew to output	$R_{DIFF} = 54\Omega$ , $C_{L1} = C_{L2} = 100pF$ (See <i>Figure 4</i> and <i>Figure 6</i> )		5	10		10	ns
t <sub>TLH</sub> t <sub>THL</sub>	Rise or fall time	$R_{DIFF} = 54\Omega$ , $C_{L1} = C_{L2} = 100pF$ (See <i>Figure 4</i> and <i>Figure 6</i> )	3	15	40	3	45	ns
t <sub>PZH</sub>	Output enable time	C <sub>L</sub> = 100pF, S2 = Closed (See <i>Figure 5</i> and <i>Figure 7</i> )		70	90		90	ns
t <sub>PZL</sub>	Output enable time	C <sub>L</sub> = 100pF, S1 = Closed (See <i>Figure 5</i> and <i>Figure 7</i> )		70	90		90	ns
t <sub>PLZ</sub>	Output disable time	C <sub>L</sub> = 15pF, S1 = Closed (See <i>Figure 5</i> and <i>Figure 7</i> )		70	90		90	ns
t <sub>PHZ</sub>	Output disable time	C <sub>L</sub> = 15pF, S2 = Closed (See <i>Figure 5</i> and <i>Figure 7</i> )		70	90		90	ns

All currents into device pins are positive; all cuttents out of device pins are negative; all voltages are referenced to device ground unless specified.

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All currents into device pins are positive; all cuttents out of device pins are negative; all voltages are referenced to device ground unless specified.

<sup>2.</sup> Supply current specification is valid for loaded transmitters when  $V_{DE} = 0V$ 

<sup>3.</sup> Applies to peak current. See typical Operating Characteristics.

Electrical characteristics ST485B - ST485C

 $V_{CC}$  = 5 V  $\pm$  5%,  $T_A$  =  $T_{MIN}$  to  $T_{MAX},$  unless otherwise specified. Typical values are referred to  $T_A$  = 25  $^{\circ}C$ 

Table 8. Receiver switching characteristics

				Value				
Symbol	Parameter	Test conditions <sup>(1)</sup>	-40 to 85 °C			-55 to 125°C		Unit
			Min.	Тур.	Max.	Min.	Max.	
t <sub>PLH</sub> t <sub>PHL</sub>	Propagation delay input to output	$R_{DIFF}$ =54 $\Omega$ , $C_{L1}$ = $C_{L2}$ = 100pF (See <i>Figure 4</i> and <i>Figure 8</i> )	20	130	210		230	ns
t <sub>SKD</sub>	Differential receiver skew	$R_{DIFF}$ =54 $\Omega$ , $C_{L1}$ = $C_{L2}$ = 100pF (See <i>Figure 4</i> and <i>Figure 8</i> )		13				ns
t <sub>PZH</sub>	Output enable time	C <sub>RL</sub> = 15pF, S1 = Closed (See Fig. 2 and <i>Figure 9</i> )		20	50		56	ns
t <sub>PZL</sub>	Output enable time	C <sub>RL</sub> = 15pF, S2 = Closed (See Fig. 2 and <i>Figure 9</i> )		20	50		56	ns
t <sub>PLZ</sub>	Output disable time	C <sub>RL</sub> = 15pF, S1 = Closed (See Fig. 2 and <i>Figure 9</i> )		20	50		56	ns
t <sub>PHZ</sub>	Output disable time	C <sub>RL</sub> = 15pF, S2 = Closed (See Fig. 2 and <i>Figure 9</i> )		20	50		56	ns
f <sub>MAX</sub>	Maximum data rate		2.5			2.5		Mbps

<sup>1.</sup> All currents into device pins are positive; all cuttents out of device pins are negative; all voltages are referenced to device ground unless specified

# 5 Test circuit and typical characteristics

Figure 2. Driver DC test load

Figure 3. Receiver timing test load

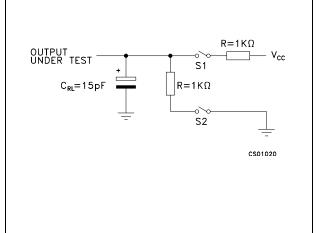


Figure 4. Drive/receiver timing test circuit

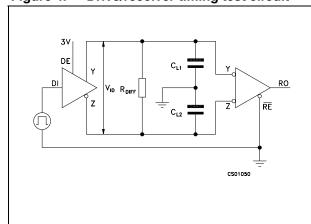


Figure 5. Driver timing test load

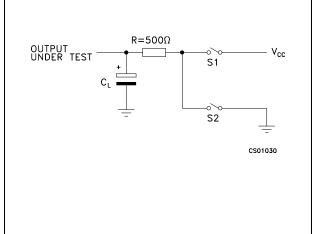


Figure 6. Driver propagation delay

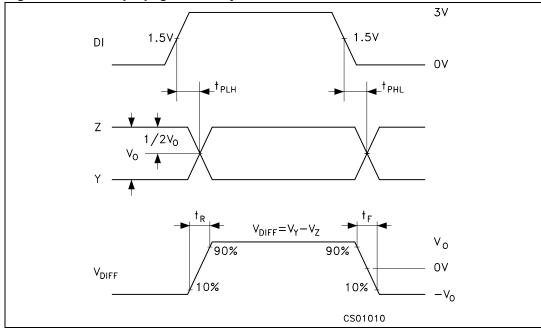
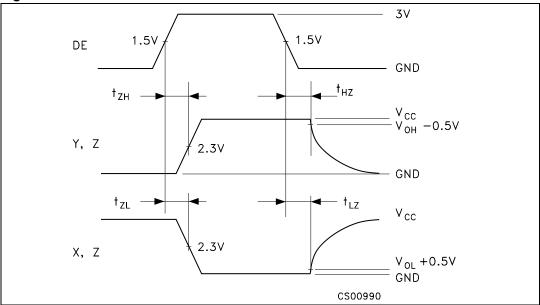


Figure 7. Driver enable and disable time



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Figure 8. Receiver propagation delay

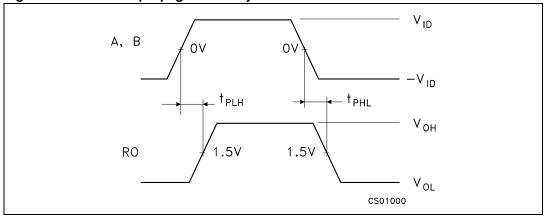


Figure 9. Receiver enable and disable time

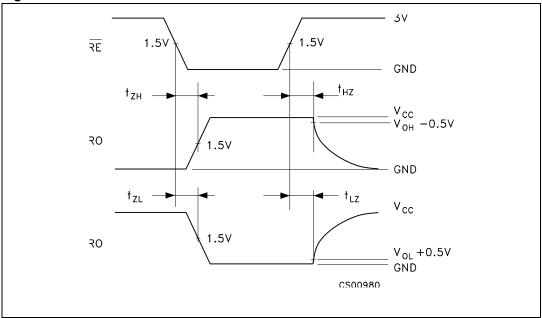


Figure 10. Receiver output current vs. output Figure 11. Receiver output current vs. output low voltage high voltage

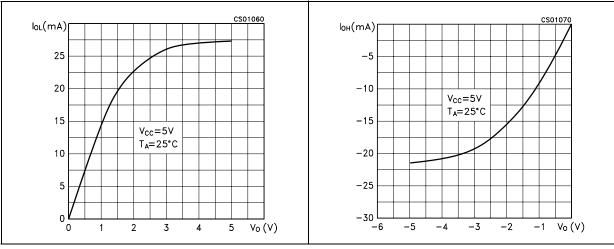


Figure 12. Driver output current vs. output low Figure 13. Driver output current vs. output voltage high voltage

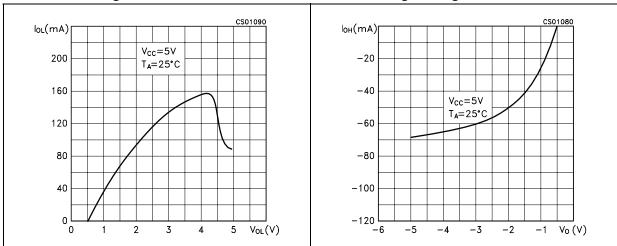
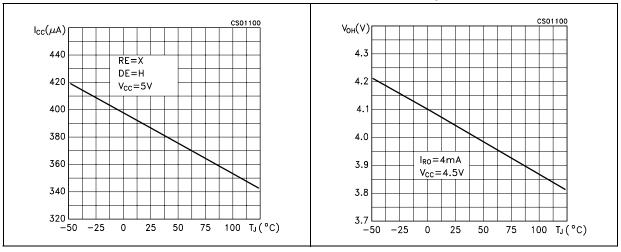


Figure 14. Supply current vs. temperature

Figure 15. Receiver high level output voltage vs. temperature

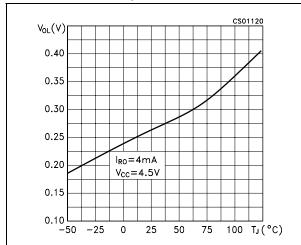


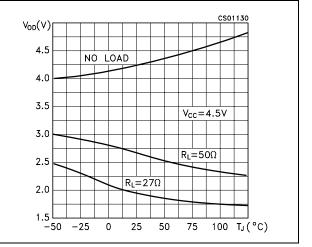
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Figure 16. Receiver low level output voltage vs. temperature

Figure 17. Differential driver output voltage vs. temperature



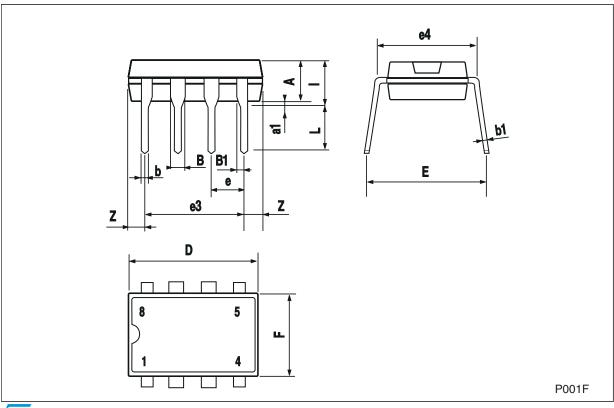


# 6 Package mechanical data

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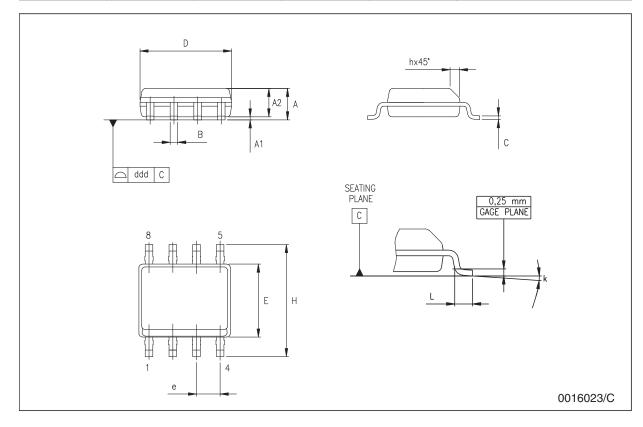
## Plastic DIP-8 mechanical data

Dim		mm.			inch.	
Dim.	Min.	Тур.	Max.	Min.	Тур.	Max.
Α		3.3			0.130	
a1	0.7			0.028		
В	1.39		1.65	0.055		0.065
B1	0.91		1.04	0.036		0.041
b		0.5			0.020	
b1	0.38		0.5	0.015		0.020
D			9.8			0.386
Е		8.8			0.346	
е		2.54			0.100	
e3		7.62			0.300	
e4		7.62			0.300	
F			7.1			0.280
I			4.8			0.189
L		3.3			0.130	
Z	0.44		1.6	0.017		0.063



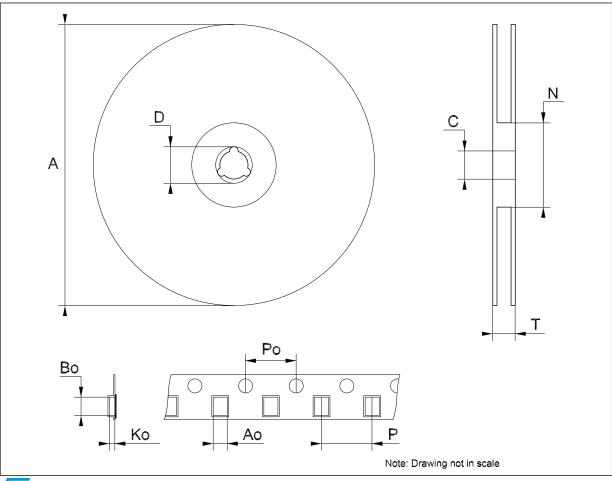
## SO-8 mechanical data

Dim	Dim.				inch.			
Dilli.	Min.	Тур.	Max.	Min.	Тур.	Max.		
Α	1.35		1.75	0.053		0.069		
A1	0.10		0.25	0.04		0.010		
A2	1.10		1.65	0.043		0.065		
В	0.33		0.51	0.013		0.020		
С	0.19		0.25	0.007		0.010		
D	4.80		5.00	0.189		0.197		
E	3.80		4.00	0.150		0.157		
е		1.27			0.050			
Н	5.80		6.20	0.228		0.244		
h	0.25		0.50	0.010		0.020		
L	0.40		1.27	0.016		0.050		
k		8° (max.)						
ddd			0.1			0.04		



Tape & reel SO-8 mechanical data

Dim.		mm. inch.		n. inch.		
Dilli.	Min.	Тур.	Max.	Min.	Тур.	Max.
А			330			12.992
С	12.8		13.2	0.504		0.519
D	20.2			0.795		
N	60			2.362		
Т			22.4			0.882
Ao	8.1		8.5	0.319		0.335
Во	5.5		5.9	0.216		0.232
Ko	2.1		2.3	0.082		0.090
Po	3.9		4.1	0.153		0.161
Р	7.9		8.1	0.311		0.319



Revision history ST485B - ST485C

# 7 Revision history

Table 9. Document revision history

Date	Revision	Changes
21-Mar-2006	12	Order codes has been updated and new template.
02-Aug-2006	13	Mistake in cover page first row mA ==> μA.
08-Nov-2006	14	Added: Table 1.
07-Feb-2008	15	Modified: Table 1 on page 1.
16-Feb-2009	16	Modified Note: on page 5.

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