## FAIRCHILD

SEMICONDUCTOR®

# FQT4N25 N-Channel QFET<sup>®</sup> MOSFET 250 V, 0.83 A, 1.75 Ω

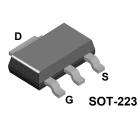
## Description

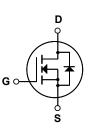
This N-Channel enhancement mode power MOSFET is produced using Fairchild Semiconductor<sup>®</sup>'s proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state resistance, and to provide superior switching performance and high avalanche energy strength. These devices are suitable for switched mode power supplies, active power factor correction (PFC), and electronic lamp ballasts.

### March 2013

Features

- + 0.83 A, 250 V,  $R_{DS(on)}$ =1.75  $\Omega(Max.)@V_{GS}$ =10 V,  $I_D$ =0.415 A
- Low Gate Charge (Typ. 4.3 nC)
- Low C<sub>rss</sub> (Typ. 4.8 pF)





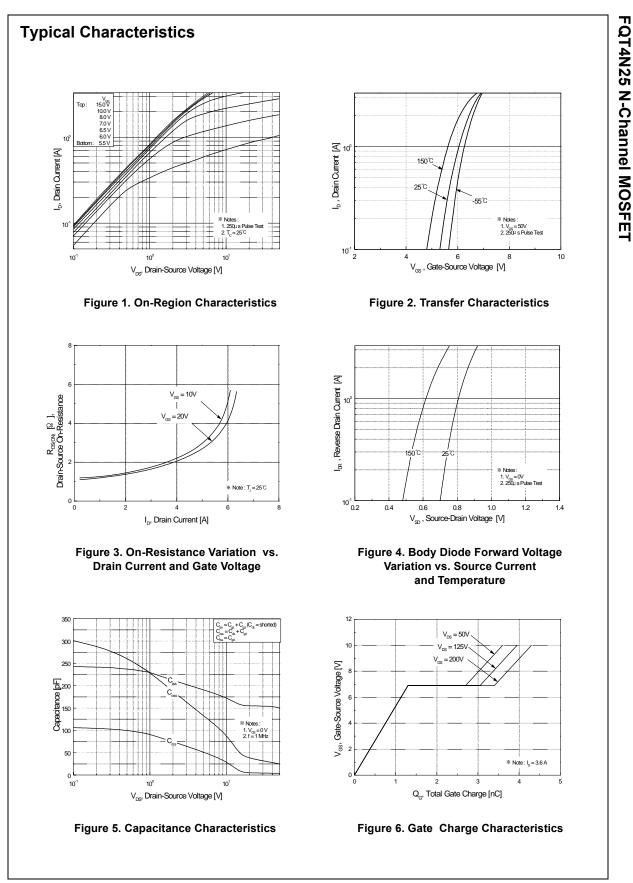
### Absolute Maximum Ratings T<sub>c</sub> = 25°C unless otherwise noted

Symbol	Parameter		FQT4N25	Unit
V <sub>DSS</sub>	Drain-Source Voltage		250	V
I <sub>D</sub>	Drain Current - Continuous (T <sub>C</sub> = 25°	°C)	0.83	А
	- Continuous (T <sub>C</sub> = 70°C)		0.66	А
I <sub>DM</sub>	Drain Current - Pulsed	(Note 1)	3.3	А
V <sub>GSS</sub>	Gate-Source Voltage		± 30	V
E <sub>AS</sub>	Single Pulsed Avalanche Energy	(Note 2)	52	mJ
I <sub>AR</sub>	Avalanche Current	(Note 1)	0.83	А
E <sub>AR</sub>	Repetitive Avalanche Energy	(Note 1)	0.25	mJ
dv/dt	Peak Diode Recovery dv/dt	(Note 3)	5.5	V/ns
PD	Power Dissipation ( $T_C = 25^{\circ}C$ )		2.5	W
	- Derate above 25°C		0.02	W/°C
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temperature Range		-55 to +150	°C
Τ <sub>L</sub>	Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds		300	°C

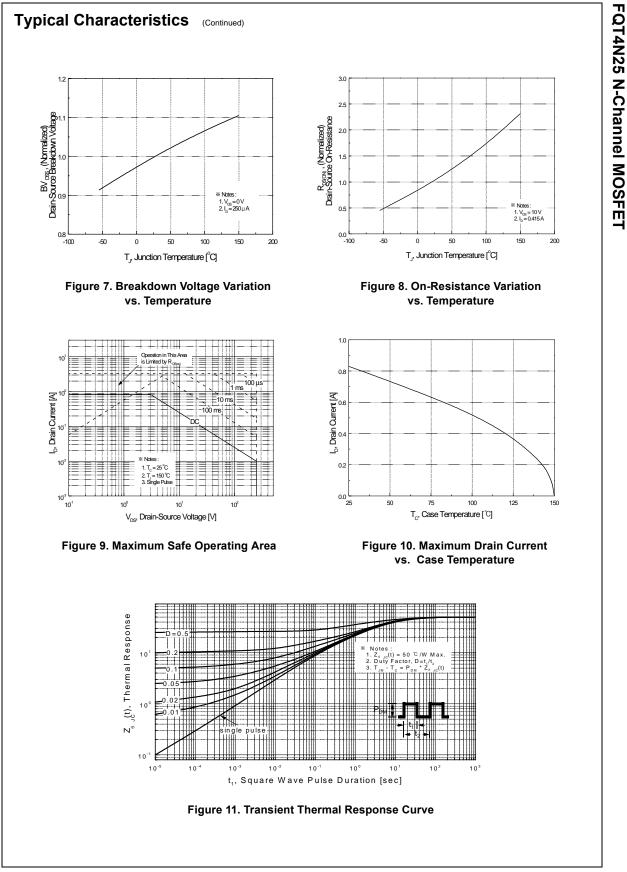
### **Thermal Characteristics**

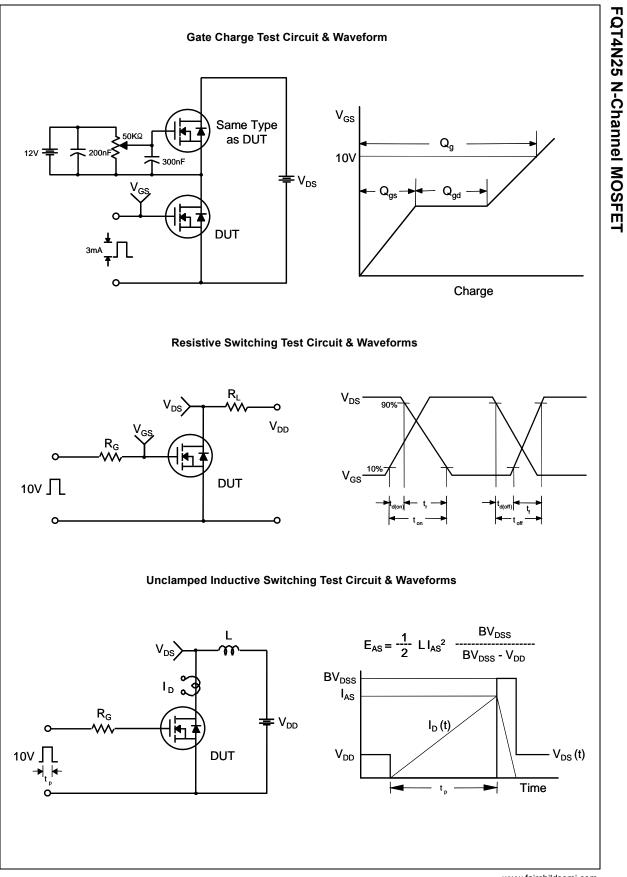
Unit	Max	Тур	Parameter	Symbol
°C/W	50		Thermal Resistance, Junction-to-Ambient *	$R_{ extsf{ heta}JA}$
	50		R <sub>0JA</sub> Thermal Resistance, Junction-to-Ambient *           * When mounted on the minimum pad size recommended (PCB Mount)	

	Parameter	Test Conditions	Min	Тур	Мах	Unit
	aracteristics					
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 250 μA	250			V
ΔBV <sub>DSS</sub> / ΔT <sub>J</sub>	Breakdown Voltage Temperature Coefficient	$I_D = 250 \ \mu$ A, Referenced to 25°C		0.22		V/°C
DSS	Zero Gate Voltage Drain Current	$V_{DS} = 250 \text{ V}, V_{GS} = 0 \text{ V}$ $V_{DS} = 200 \text{ V}, T_C = 125^{\circ}\text{C}$			1 10	μΑ μΑ
GSSF	Gate-Body Leakage Current, Forward	$V_{GS} = 30 \text{ V}, V_{DS} = 0 \text{ V}$			100	nA
GSSR	Gate-Body Leakage Current, Reverse	$V_{GS} = -30 \text{ V}, \text{ V}_{DS} = 0 \text{ V}$			-100	nA
			I			
V <sub>GS(th)</sub>	aracteristics Gate Threshold Voltage	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250 μA	3.0		5.0	V
R <sub>DS(on)</sub>	Static Drain-Source On-Resistance	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 0.415 \text{ A}$		1.38	1.75	Ω
Ĵfs	Forward Transconductance	V <sub>DS</sub> = 50 V, I <sub>D</sub> = 0.415 A (Note 4)		1.28		S
Dynam	ic Characteristics					
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0 V,		155	200	pF
C <sub>oss</sub>	Output Capacitance	f = 1.0 MHz		35	45	pF
C <sub>rss</sub>	Reverse Transfer Capacitance			4.8	6.5	pF
	Ing Characteristics			6.8	25	20
d(on) r	Turn-On Rise Time	$V_{DD} = 125 \text{ V}, \text{ I}_{D} = 3.6 \text{ A},$		45	100	ns ns
d(off)	Turn-Off Delay Time	$R_{G} = 25 \Omega$		6.4	25	ns
f	Turn-Off Fall Time	(Note 4, 5)		22	55	ns
ຊ <sub>g</sub>	Total Gate Charge	V <sub>DS</sub> = 200 V, I <sub>D</sub> = 3.6 A,		4.3	5.6	nC
ସୁ <sub>gs</sub>	Gate-Source Charge	-		1.3		nC
ວ ວ <sub>gd</sub>	Gate-Drain Charge	(Note 4, 5)		2.1		nC
	Source Diode Characteristics ar				0.93	•
						A
C14						× V
	Drain Oburce Diode i ofward Voltage	$V_{GS} = 0 V, I_S = 3.6 A,$		110		ns
SM V <sub>SD</sub>	Reverse Recovery Time	V c c = U V, $l c = 0.0 A$ .				110
Q <sub>gd</sub> Drain-S	Gate-Drain Charge	nd Maximum Ratings bde Forward Current Forward Current $V_{GS} = 0 V, I_S = 0.83 A$	  			

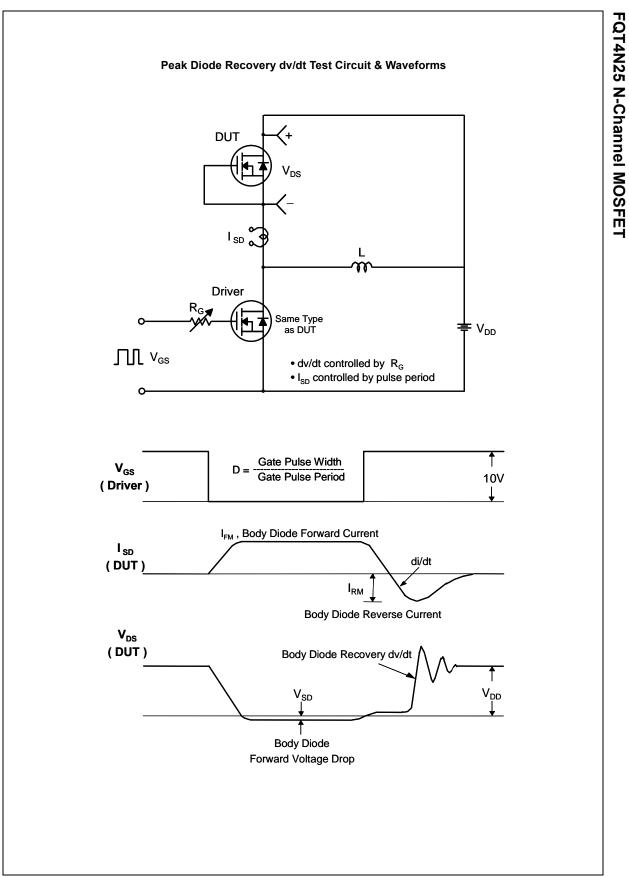


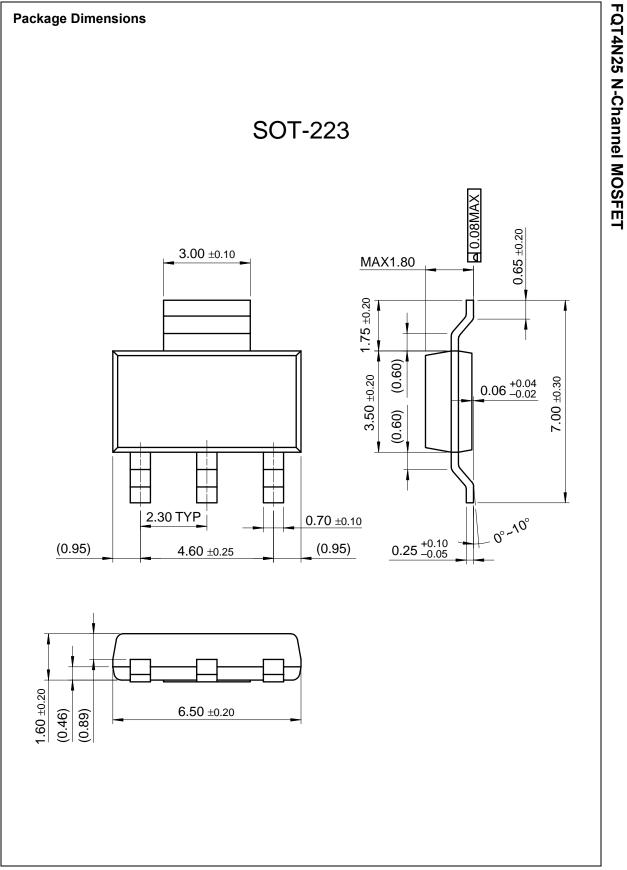
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