

ACS102-6T

Transient protected AC switch (ACS™)

Features

- Needs no external protection snubber or varistor
- Enables equipment to meet IEC 61000-4-5
- Reduces component count by up to 80%
- Interfaces directly with the microcontroller
- Common package tab connection supports connection of several alternating current switches (ACS) on the same cooling pad
- Integrated structure based on ASD technology
- Overvoltage protection by crowbar technology
- High noise immunity static dV/dt > 300 V/µs

Applications

- Alternating current on/off static switching in appliances and industrial control systems
- Drive of low-power, high-inductive or resistive loads like:
 - relay, valve, solenoid
 - dispenser, door lock
 - micro-motor

Description

The ACS102-6T belongs to the AC line switch family. This high performance switch can control a load of up to 0.2A.

The ACS102-6T switch includes an overvoltage crowbar structure to absorb the overvoltage energy, and a gate level shifter driver to separate the digital controller from the main switch. It is triggered with a negative gate current flowing out of the gate pin.

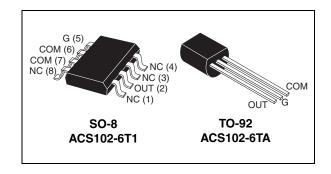


Figure 1. Functional diagram

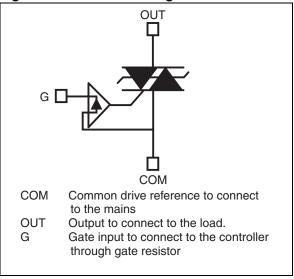


Table 1. Device summary

Symbol	Value	Unit
I _{T(RMS)}	0.2	Α
V_{DRM}/V_{RRM}	600	V
I _{GT}	5	mA

TM: ACS is a trademark of STMicroelectronics ASD: Application specific devices

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Table 2. Absolute maximum ratings (T_{amb} = 25 °C, unless otherwise specified)

Symbol	Paramete	Value	Unit		
1	On-state rms current (full sine wave)	TO-92	T _{amb} = 100 °C	0.2	Α
I _{T(RMS)}		SO-08	T _{amb} = 100 °C	0.2	_ ^
	Non repetitive surge peak on-state current	f = 60 Hz	t = 16.7 ms	7.6	Α
ITSM	(full cycle sine wave, T _j initial = 25 °C)	f = 50 Hz	t = 20 ms	7.3	Α
l²t	I ² t Value for fusing	for fusing $t_p = 10 \text{ ms}$			A²s
dl/dt	Critical rate of rise of on-state current $I_G = 2xI_{GT}$, tr ≤ 100 ns		T _j = 125 °C	50	A/µs
V _{PP}	Non repetitive line peak mains voltage ⁽¹⁾	T _j = 25 °C	2	kV	
I _{GM}	Peak gate current $t_p = 20 \mu s$		T _j = 125 °C	1	Α
V_{GM}	Peak positive gate voltage $T_j = 125 ^{\circ}\text{C}$			10	٧
P _{G(AV)}	Average gate power dissipation	T _j = 125 °C	0.1	W	
T _{stg} T _j	Storage junction temperature range Operating junction temperature range			-40 to +150 -30 to +125	°C

^{1.} According to test described by IEC 61000-4-5 standard and Figure 17

Table 3. Electrical characteristics ($T_j = 25$ °C, unless otherwise specified)

Symbol	Test conditions Quadrant			Value	Unit
I _{GT} ⁽¹⁾	V - 12 V B - 22 O	II - III	MAX	5	mA
V _{GT}	$V_{OUT} = 12 \text{ V, R}_{L} = 33 \Omega$	II - III	MAX	0.9	V
V_{GD}	$V_{OUT} = V_{DRM}$, $R_L = 3.3 \text{ k}\Omega$, $T_j = 125 \text{ °C}$	MIN	0.15	V	
I _H ⁽²⁾	I _{OUT} = 100 mA	MAX	20	mA	
I _L ⁽²⁾	$I_G = 1.2 \times I_{GT}$	MAX	25	mA	
dV/dt (2)	V _{OUT} = 67% V _{DRM} , gate open, T _j = 125 °C		MIN	300	V/µs
(dl/dt)c (2)	Without snubber (15 V/ μ s), turn-off time \leq 20 ms, T	MIN	0.15	A/ms	
V _{CL}	$I_{CL} = 0.1 \text{ mA}, t_p = 1 \text{ ms}, T_j = 125 ^{\circ}\text{C}$		MIN	650	V

^{1.} Minimum $I_{\mbox{\scriptsize GT}}$ is guaranteed at 10% of $I_{\mbox{\scriptsize GT}}$ max

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^{2.} For both polarities of OUT referenced to COM

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Table 4. Static electrical characteristics

Symbol	Test co	Value	Unit		
V _{TM} ⁽¹⁾	$I_{TM} = 0.3 \text{ A}, t_p = 380 \ \mu\text{s}$	Tj = 25 °C	MAX	1.2	V
V _{TO} (1)		Tj = 125 °C	MAX	0.80	V
R _D ⁽¹⁾		Tj = 125 °C	MAX	500	mΩ
I _{DRM}	V. – 600 V	Tj = 25 °C	MAX	2	μΑ
I _{RRM}	V _{OUT} = 600 V	Tj = 125 °C	IVIAA	0.2	mA

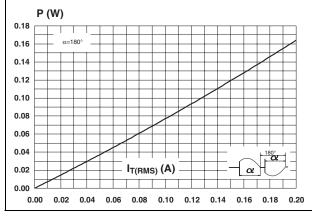
^{1.} for both polarities of OUT referenced to COM

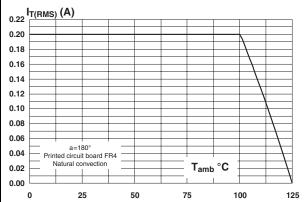
Table 5. Thermal resistance

Symbol	Par	Value	Unit		
R _{th (j-l)}	Junction to lead (AC)		TO-92	60	
R _{th (j-a)}	Junction to ambient		TO-92	150	°C/W
	Junction to ambient	S = 40 mm ²	SO-8	150	

Figure 2. Maximum power dissipation versus on-state rms current (full cycle)

Figure 3. On-state rms current versus ambient temperature (full cycle)





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Figure 4. Relative variation of junction to ambient thermal impedance versus pulse duration and package

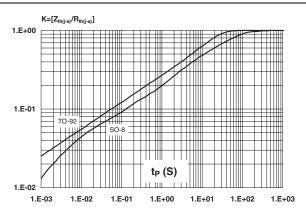


Figure 5. Relative variation of gate trigger, holding and latching current versus junction temperature

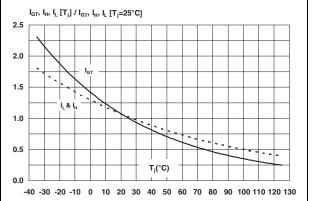
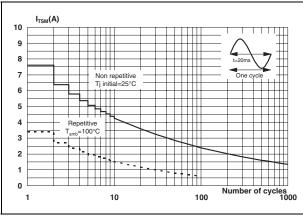


Figure 6. Non repetitive surge peak on-state current versus number of cycles

Figure 7. Non repetitive surge peak on-state current for a sinusoidal pulse, and corresponding value of l²t



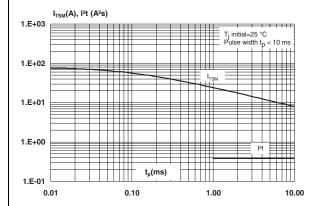
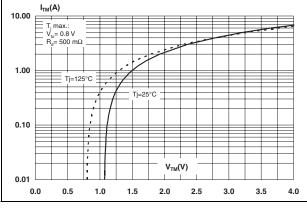
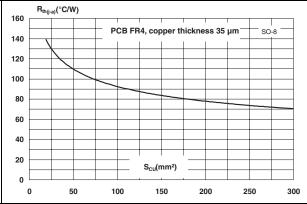


Figure 8. On-state characteristics (maximal values)

Figure 9. SO-8 junction to ambient thermal resistance versus copper surface under tab





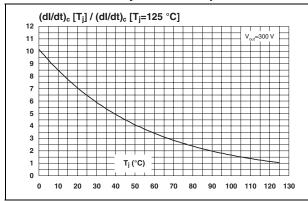
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Figure 10. Relative variation of critical rate of decrease of main current (di/dt)c versus junction temperature

Figure 11. Relative variation of critical rate of decrease of main current (di/dt)c versus (dV/dt)c



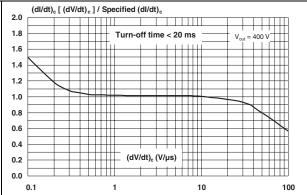
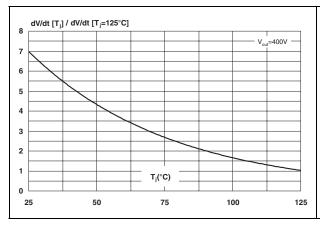
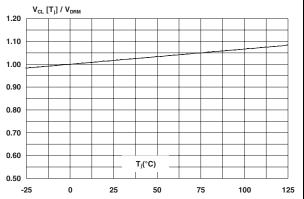


Figure 12. Relative variation of static dV/dt versus junction temperature

Figure 13. Relative variation of the maximal clamping voltage versus junction temperature (min value)



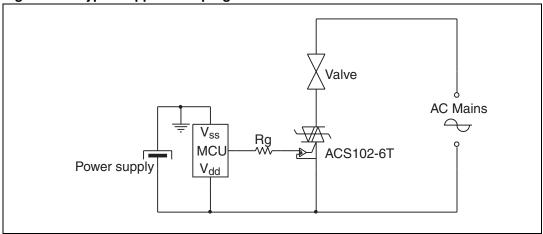


2 Alternating current line switch - basic application

The ACS102-6T switch is triggered by a negative gate current flowing from the gate pin G. The switch can be driven directly by the digital controller through a resistor as shown in *Figure 14*.

Thanks to its overvoltage protection and turn-off commutation performance, the ACS102-6T switch can drive a small power, high-inductive load with neither varistor nor additional turn-off snubber.

Figure 14. Typical application program



2.1 Protection against overvoltage: the best choice is ACS

In comparison with standard TRIACs, which are not robust against surge voltage, the ACS102-6T is overvoltage self-protected, specified by the new parameter V_{CL} . This feature is useful in two operating conditions: in case of turn-off of very inductive load, and in case of surge voltage that can occur on the electrical network.

2.1.1 High inductive load switch-off: turn-off overvoltage clamping

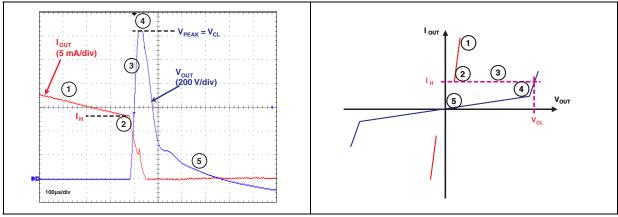
With high inductive and low rms current loads the rate of decrease of the current is very low. An overvoltage can occur when the gate current is removed and the OUT current is lower than I_H .

As shown in *Figure 15* and *Figure 16*, at the end of the last conduction half cycle, the load current decreases (1). The load current reaches the holding current level I_H (2), and the ACS turns off (3). The water valve, as an inductive load (up to 15 H), reacts as a current generator and an overvoltage is created, which is clamped by the ACS (4). The current flows through the ACS avalanche and decreases linearly to zero. During this time, the voltage across the switch is limited to the clamping voltage V_{CL} . The energy stored in the inductance of the load is dissipated in the clamping section that is designed for this purpose. When the energy has been dissipated, the ACS voltage falls back to the mains voltage value (5).

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Figure 15. Effect of the switching off of a high Figure 16. inductive load - typical clamping capability of ACS102-6T

Description of the different steps during switching off of a high inductive load



2.1.2 Alternating current line transient voltage ruggedness

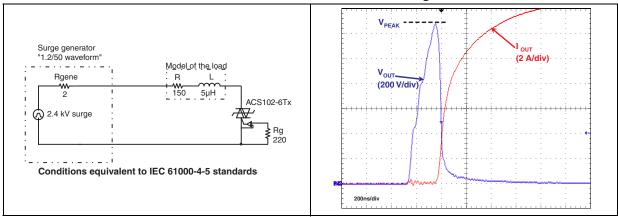
The ACS102-6T switch is able to withstand safely the AC line transients either by clamping the low energy spikes or by breaking over under high energy shocks, even with high turn-on current rise.

The test circuit shown in *Figure 17* is representative of the final ACS102-6T application, and is also used to test the ACS switch according to the IEC 61000-4-5 standard conditions. Thanks to the load limiting the current, the ACS102-6T switch withstands the voltage spikes up to 2 kV above the peak line voltage. The protection is based on an overvoltage crowbar technology. Actually, the ACS102-6T breaks over safely as shown in *Figure 18*. The ACS102-6T recovers its blocking voltage capability after the surge (switch off back at the next zero crossing of the current).

Such non-repetitive tests can be done 10 times on each AC line voltage polarity.

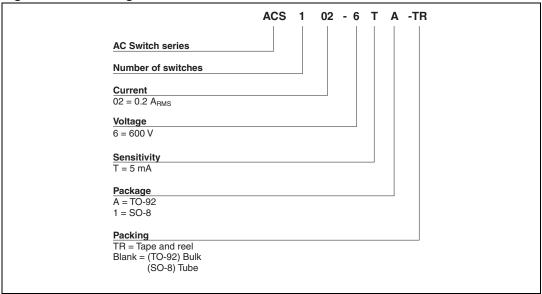
Figure 17. Overvoltage ruggedness test circuit Figure 18. Typical current and voltage for resistive and inductive loads waveforms across the ACS

Typical current and voltage waveforms across the ACS102-6T during IEC 61000-4-5 standard test



3 Ordering information scheme

Figure 19. Ordering information scheme



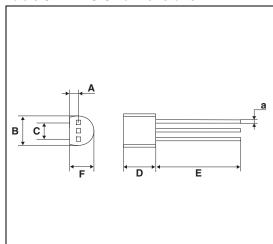
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4 Package information

- Epoxy meets UL94, V0
- Lead-free packages

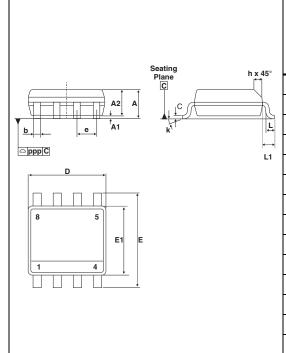
In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK® specifications, grade definitions and product status are available at: www.st.com. ECOPACK® is an ST trademark.

Table 6. TO-92 dimensions



	Dimensions					
Ref	Millimeters		rs	Inches		
	Min.	Тур.	Max.	Min.	Тур.	Max.
Α		1.35			0.053	
В			4.70			0.185
С		2.54			0.100	
D	4.40			0.173		
Е	12.70			0.500		
F			3.70			0.146
а			0.50			0.019

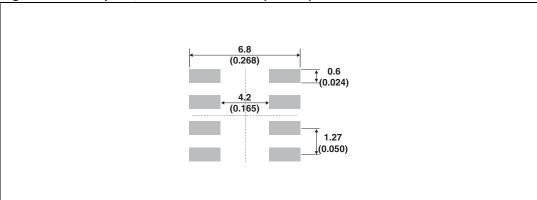
Table 7. SO-8 dimensions



	Dimensions							
Ref.	ef. Millimeters Inches							
	Min.	Тур.	Max.	Min.	Тур.	Max.		
Α			1.75			0.069		
A1	0.1		0.25	0.004		0.010		
A2	1.25			0.049				
b	0.28		0.48	0.011		0.019		
С	0.17		0.23	0.007		0.009		
D	4.80	4.90	5.00	0.189	0.193	0.197		
E	5.80	6.00	6.20	0.228	0.236	0.244		
E1	3.80	3.90	4.00	0.150	0.154	0.157		
е		1.27			0.050			
h	0.25		0.50	0.010		0.020		
L	0.40		1.27	0.016		0.050		
L1		1.04			0.041			
k	0°		8°	0°		8°		
ppp			0.10			0.004		

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Figure 20. Footprint, dimensions in mm (inches)



5 Ordering information

Table 8. Ordering information

Order code	Marking	Package	Weight	Base qty	Packing mode
ACS102-6TA	ACS1026T	TO-92	0.2 g	2500	Bulk
ACS102-6TA-TR	ACS1026T	TO-92	0.2 g	2000	Tape and reel
ACS102-6T1	ACS1026T	SO-8	0.11 g	100	Tube
ACS102-6T1-TR	ACS1026T	SO-8	0.11 g	2500	Tape and reel

6 Revision history

Table 9. Document revision history

Date	Revision	Changes
05-Jan-2006	1	Initial release.
07-Jun-2006	2	Reformatted to current standards. Replaced Figure 9.
24-May-2011	3	Added pin indications on first page. Corrected dimensions in Table 7.

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