

October 2014

CNY171M, CNY172M, CNY173M, CNY174M, CNY17F1M, CNY17F2M, CNY17F3M, CNY17F4M, MOC8106M 6-Pin DIP High BV_{CEO} Phototransistor Optocouplers

Features

- High BV_{CEO}: 70 V Minimum (CNY17XM, CNY17FXM, MOC8106M)
- Closely Matched Current Transfer Ratio (CTR)
 Minimizes Unit-to-Unit Variation
- Current Transfer Ratio In Select Groups
- Very Low Coupled Capacitance Along With No Chip-to-Pin 6 Base Connection for Minimum Noise Susceptability (CNY17FXM, MOC8106M)
- Safety and Regulatory Approvals:
 - UL1577, 4,170 VAC_{RMS} for 1 Minute
 - DIN-EN/IEC60747-5-5, 850 V Peak Working Insulation Voltage

Applications

- Power Supply Regulators
- Digital Logic Inputs
- Microprocessor Inputs
- Appliance Sensor Systems
- Industrial Controls

Description

The CNY17XM, CNY17FXM, and MOC8106M devices consist of a gallium arsenide infrared emitting diode coupled with an NPN phototransistor in a dual in-line package.

Package Outlines

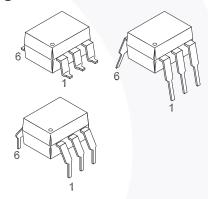


Figure 1. Package Outlines

Schematics

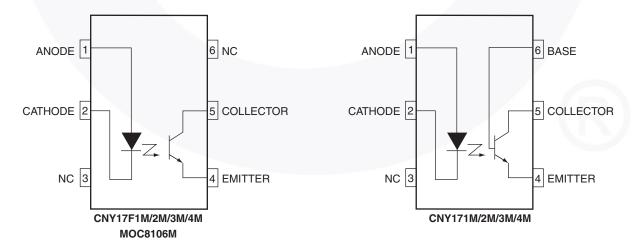


Figure 2. Schematics

Safety and Insulation Ratings

As per DIN EN/IEC 60747-5-5, this optocoupler is suitable for "safe electrical insulation" only within the safety limit data. Compliance with the safety ratings shall be ensured by means of protective circuits.

Parameter	Characteristics	
Installation Classifications per DIN VDE	lation Classifications per DIN VDE < 150 V _{RMS}	
0110/1.89 Table 1, For Rated Mains Voltage	< 300 V _{RMS}	I–IV
Climatic Classification		55/100/21
Pollution Degree (DIN VDE 0110/1.89)		2
Comparative Tracking Index		175

Symbol	Parameter	Value	Unit
\/	Input-to-Output Test Voltage, Method A, $V_{IORM} \times 1.6 = V_{PR}$, Type and Sample Test with $t_m = 10$ s, Partial Discharge < 5 pC	1360	V _{peak}
V _{PR}	Input-to-Output Test Voltage, Method B, V _{IORM} x 1.875 = V _{PR} , 100% Production Test with t _m = 1 s, Partial Discharge < 5 pC	1594	V _{peak}
V _{IORM}	Maximum Working Insulation Voltage	850	V _{peak}
V _{IOTM}	Highest Allowable Over-Voltage	6000	V _{peak}
	External Creepage	≥ 7	mm
	External Clearance	≥ 7	mm
	External Clearance (for Option TV, 0.4" Lead Spacing)	≥ 10	mm
DTI	Distance Through Insulation (Insulation Thickness)	≥ 0.5	mm
T _S	Case Temperature ⁽¹⁾	175	°C
I _{S,INPUT}	Input Current ⁽¹⁾	350	mA
P _{S,OUTPUT}	Output Power ⁽¹⁾	800	mW
R _{IO}	Insulation Resistance at T _S , V _{IO} = 500 V ⁽¹⁾	> 10 ⁹	Ω

Note:

1. Safety limit values – maximum values allowed in the event of a failure.

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Parameters	Value	Units
TOTAL DE	VICE		
T _{STG}	Storage Temperature	-40 to +125	°C
T _A	Ambient Operating Temperature	-40 to +100	°C
T _J	Junction Temperature	-40 to +125	°C
T _{SOL}	Lead Solder Temperature	260 for 10 seconds	°C
	Total Device Power Dissipation @ 25°C (LED plus detector)	270	mW
P_{D}	Derate Linearly From 25°C	2.94	mW/°C
EMITTER			
I _F	Continuous Forward Current	60	mA
V _R	Reverse Voltage	6	V
I _F (pk)	Forward Current – Peak (1 µs pulse, 300 pps)	1.5	Α
Б	LED Power Dissipation 25°C Ambient	120	mW
P_{D}	Derate Linearly From 25°C	1.41	mW/°C
DETECTO	R		
I _C	Continuous Collector Current	50	mA
V _{CEO}	Collector-Emitter Voltage	70	V
V _{ECO}	Emitter Collector Voltage	7	V
	Detector Power Dissipation @ 25°C	150	mW
P_{D}	Derate Linearly from 25°C	1.76	mW/°C

Electrical Characteristics

 $T_A = 25$ °C unless otherwise specified.

Individual Component Characteristics

Symbol	Parameters	Test Conditions	Device	Min.	Тур.	Max.	Units
EMITTER				•	1		
		I _F = 10 mA	All Devices	1.0	1.15	1.50	V
V_{F}	Input Forward Voltage	I _F = 60 mA	CNY17XM, CNY17FXM	1.0	1.35	1.65	V
CJ	Capacitance	$V_F = 0 V, f = 1.0 MHz$	All Devices		18		pF
I _R	Reverse Leakage Current	V _R = 6 V	All Devices		0.001	10	μA
DETECTO	OR			•	•	•	
	Breakdown Voltage						
BV_CEO	Collector-to-Emitter	$I_C = 1 \text{ mA}, I_F = 0$	All Devices	70	100		V
BV _{CBO}	Collector-to-Base	I _C = 10 μA, I _F = 0	CNY17XM	70	120		V
BV _{ECO}	Emitter-to-Collector	$I_E = 100 \mu A, I_F = 0$	All Devices	7	10		V
1/1	Leakage Current			4			
I_{CEO}	Collector-to-Emitter	$V_{CE} = 10 \text{ V}, I_{F} = 0$	All Devices		1	50	nA
I _{CBO}	Collector-to-Base	V _{CB} = 10 V, I _F = 0	CNY17XM			20	nA
	Capacitance						
C_{CE}	Collector-to-Emitter	V _{CE} = 0, f = 1 MHz	All Devices		8		pF
C _{CB}	Collector-to-Base	V _{CB} = 0, f = 1 MHz	CNY17XM		20		pF
C _{EB}	Emitter-to-Base	V _{EB} = 0, f = 1 MHz	CNY17XM		10		pF

Transfer Characteristics

Symbol	Parameters	Test Conditions	Device		Тур.	Max.	Units
COUPLE	D					•	
	CTR Current Transfer	I _F = 10 mA, V _{CE} = 10 V	MOC8106M	50		150	%
CTR Current Trans Ratio		I _F = 10 mA, V _{CE} = 5 V	CNY171M, CNY17F1M	40		80	%
		I _F = 10 mA, V _{CE} = 5 V	CNY172M, CNY17F2M	63		125	%
	riado	I _F = 10 mA, V _{CE} = 5 V	CNY173M, CNY17F3M	100		200	%
		I _F = 10 mA, V _{CE} = 5 V	CNY174M, CNY17F4M	160		320	%
V _{CE(SAT)}	Collector-Emitter Saturation Voltage	$I_C = 0.5 \text{ mA}, I_F = 5 \text{ mA}$	MOC8106M	8106M		0.4	V
		$I_C = 2.5 \text{ mA}, I_F = 10 \text{ mA}$	CNY17XM/CNY17FXM		0.4		\ \ \

Electrical Characteristics (Continued)

 $T_A = 25$ °C unless otherwise specified.

AC Characteristics

Symbol	Parameters	Test Conditions	Device	Min.	Тур.	Max.	Units	
NON-SAT	NON-SATURATED SWITCHING TIME							
t _{on}	Turn-On Time	I_C = 2.0 mA, V_{CC} = 10 V, R_L = 100 Ω	All Devices		2.0	10.0	μs	
t _{off}	Turn-Off Time	I_C = 2.0 mA, V_{CC} = 10 V, R_L = 100 Ω	All Devices		3.0	10.0	μs	
t _d	Delay Time	$I_F = 10 \text{ mA}, V_{CC} = 5 \text{ V}, R_L = 75 \Omega$	CNY17XM/CNY17FXM			5.6	μs	
t _r	Rise Time	I_F = 10 mA, V_{CC} = 5 V, R_L = 75 Ω	CNY17XM/CNY17FXM			4.0	μs	
ts	Storage Time	$I_F = 10 \text{ mA}, V_{CC} = 5 \text{ V}, R_L = 75 \Omega$	CNY17XM/CNY17FXM			4.1	μs	
t _f	Fall Time	$I_F = 10 \text{ mA}, V_{CC} = 5 \text{ V}, R_L = 75 \Omega$	CNY17XM/CNY17FXM			3.5	μs	
SATURAT	TED SWITCHING	TIMES				•		
		$I_F = 20 \text{ mA}, V_{CC} = 5 \text{ V}, R_L = 1 \text{ k}\Omega$	CNY171M/F1M			5.5	μs	
t _d	Delay Time	$I_F = 10 \text{ mA}, V_{CC} = 5 \text{ V}, R_L = 1 \text{ k}\Omega$	CNY172M/3M/4M CNY17F2M/F3M/F4M			8.0	μs	
		$I_F = 20 \text{ mA}, V_{CC} = 5 \text{ V}, R_L = 1 \text{ k}\Omega$	CNY171M/F1M			4.0	μs	
t _r	Rise Time	$I_F = 10 \text{ mA}, V_{CC} = 5 \text{ V}, R_L = 1 \text{ k}\Omega$	CNY172M/3M/4M CNY17F2M/F3M/F4M			6.0	μs	
	The state of the s	$I_F = 20 \text{ mA}, V_{CC} = 5 \text{ V}, R_L = 1 \text{ k}\Omega$	CNY171M/F1M			34.0	μs	
t _s	Storage Time	$I_F = 10 \text{ mA}, V_{CC} = 5 \text{ V}, R_L = 1 \text{ k}\Omega$	CNY172M/3M/4M CNY17F2M/F3M/F4M			39.0	μs	
		I_F = 20 mA, V_{CC} = 5 V, R_L = 1 kΩ	CNY171M/F1M			20.0	μs	
t _f	Fall Time	$I_F = 10 \text{ mA}, V_{CC} = 5 \text{ V}, R_L = 1 \text{ k}\Omega$	CNY172M/3M/4M CNY17F2M/F3M/F4M			24.0	μs	

Isolation Characteristics

Symbol	Characteristic	Test Conditions	Min.	Тур.	Max.	Units
V _{ISO}	Input-Output Isolation Voltage	t = 1 Minute	4170			VAC _{RMS}
C _{ISO}	Isolation Capacitance	V _{I-O} = 0 V, f = 1 MHz		0.2		pF
R _{ISO}	Isolation Resistance	$V_{I-O} = \pm 500 \text{ VDC}, T_A = 25^{\circ}\text{C}$	10 ¹¹			Ω

Typical Performance Characteristics

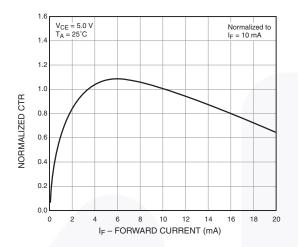


Figure 3. Normalized CTR vs. Forward Current

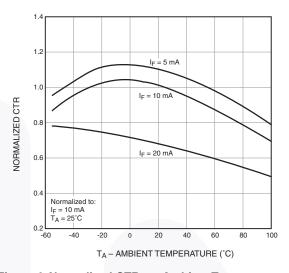


Figure 4. Normalized CTR vs. Ambient Temperature

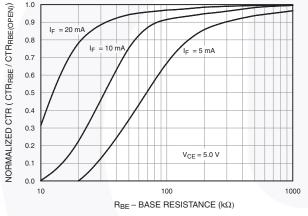


Figure 5. CTR vs. RBE (Unsaturated)

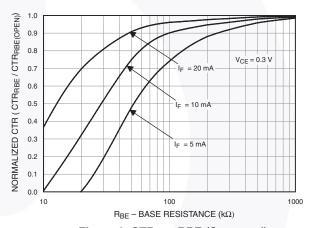


Figure 6. CTR vs. RBE (Saturated)

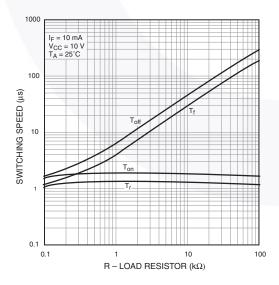


Figure 7. Switching Speed vs. Load Resistor

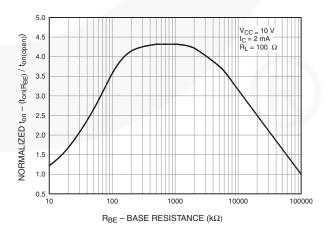
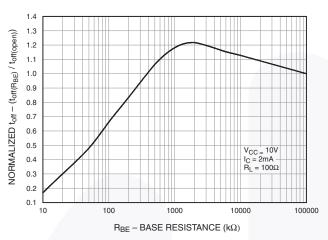


Figure 8. Normalized ton vs. RBE

Typical Performance Characteristics (Continued)



1.8

1.7

2 1.6

HSW 1.5

1.5

1.7

TA = -55°C

TA = 100°C

1.1

1.0

1 10

1 100

IF - LED FORWARD CURRENT (mA)

Figure 9. Normalized toff vs. RBE

Figure 10. LED Forward Voltage vs. Forward Current

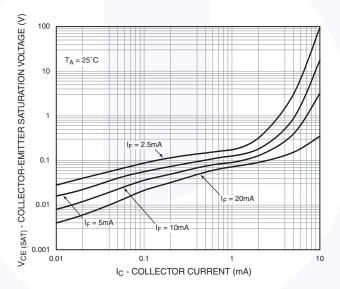


Figure 11. Collector-Emitter Saturation Voltage vs. Collector Current

Switching Test Circuit and Waveforms

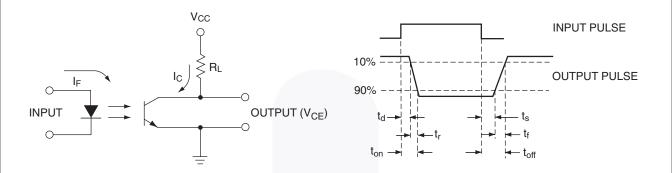


Figure 12. Switching Test Circuit and Waveforms

Reflow Profile

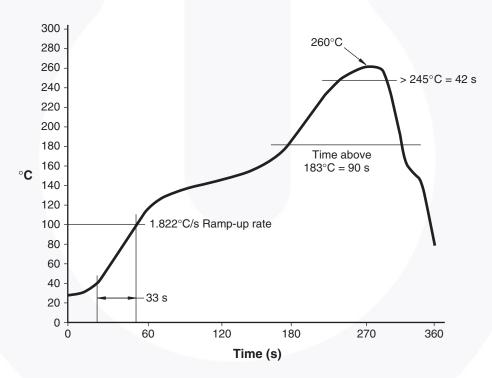


Figure 13. Reflow Profile

Ordering Information

Part Number	Package	Packing Method
CNY171M	DIP 6-Pin	Tube (50 Units)
CNY171SM	SMT 6-Pin (Lead Bend)	Tube (50 Units)
CNY171SR2M	SMT 6-Pin (Lead Bend)	Tape and Reel (1000 Units)
CNY171TM	DIP 6-Pin, 0.4" Lead Spacing	Tube (50 Units)
CNY171VM	DIP 6-Pin, DIN EN/IEC60747-5-5 Option	Tube (50 Units)
CNY171SVM	SMT 6-Pin (Lead Bend), DIN EN/IEC60747-5-5 Option	Tube (50 Units)
CNY171SR2VM	SMT 6-Pin (Lead Bend), DIN EN/IEC60747-5-5 Option	Tape and Reel (1000 Units)
CNY171TVM	DIP 6-Pin, 0.4" Lead Spacing, DIN EN/IEC60747-5-5 Option	Tube (50 Units)

Note:

2. The product orderable part number system listed in this table also applies to the CNY17FXM product family and the MOC8106M device.

Marking Information

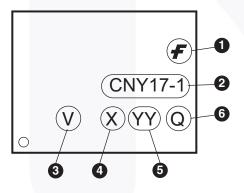
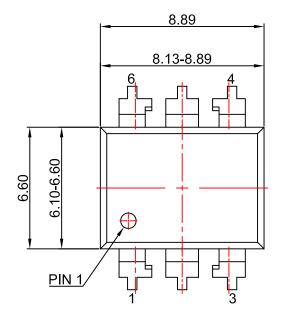
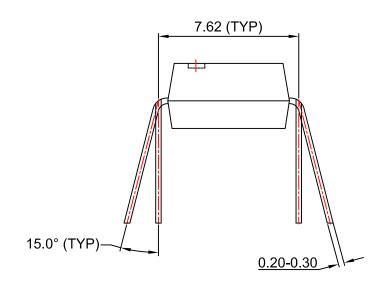


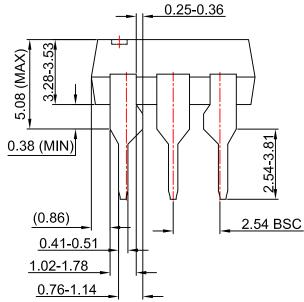
Figure 14. Top Mark

Table 1. Top Mark Definitions

1	Fairchild Logo
2	Device Number
3	DIN EN/IEC60747-5-5 Option (only appears on component ordered with this option)
4	One-Digit Year Code, e.g., "4"
5	Digit Work Week, Ranging from "01" to "53"
6	Assembly Package Code



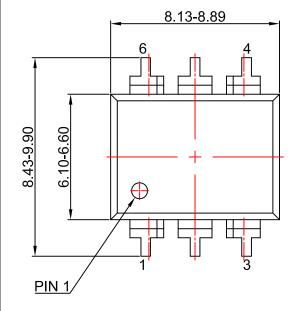


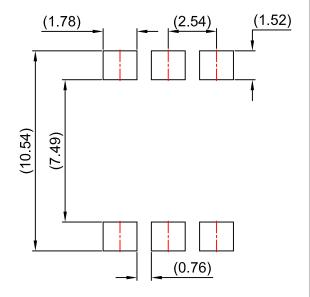


NOTES:

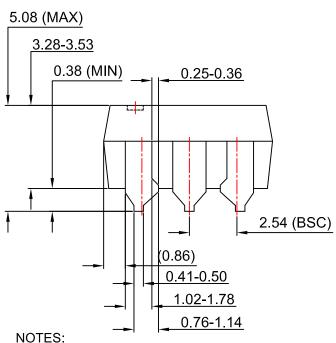
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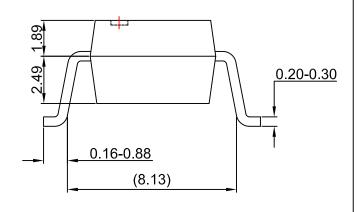






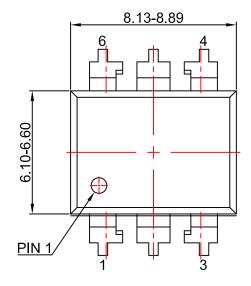
LAND PATTERN RECOMMENDATION

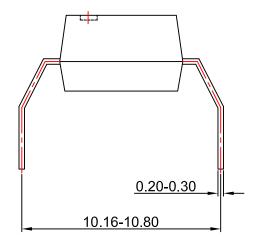


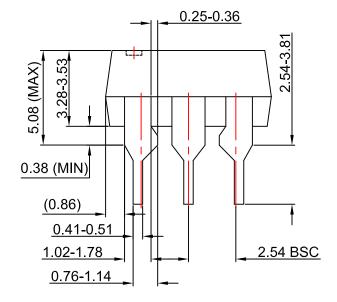


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Definition of Terms		
Datasheet Identification		Definition
Advance Information	Formative / In Design	Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
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Rev. 172

AMEYA360 Components Supply Platform

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Contact Us:

> Address:

401 Building No.5, JiuGe Business Center, Lane 2301, Yishan Rd Minhang District, Shanghai , China

> Sales:

Direct +86 (21) 6401-6692

Email amall@ameya360.com

QQ 800077892

Skype ameyasales1 ameyasales2

Customer Service :

Email service@ameya360.com

Partnership :

Tel +86 (21) 64016692-8333

Email mkt@ameya360.com