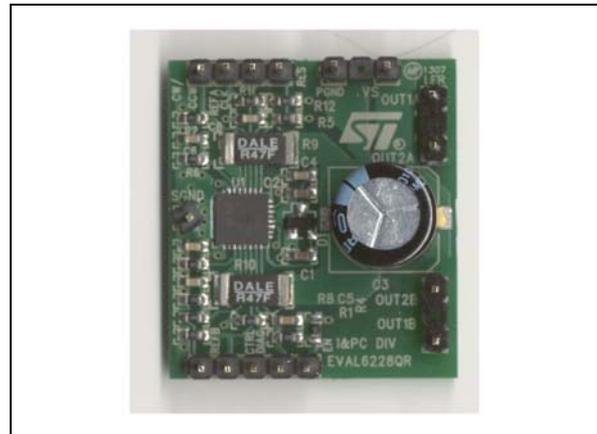


Demonstration board mounting the L6228Q dual full-bridge driver

Data brief

Features

- Operating supply voltage from 8 to 52 V
- 2.8 A output peak current (1.4 A_{r.m.s.})
- R_{DS(on)} 0.73 Ω typ. value @ T_J = 25 °C
- Operating frequency up to 100 kHz
- Non dissipative overcurrent protection
- Dual independent constant t_{OFF} PWM current controllers
- Fast/slow decay mode selection
- Fast decay quasi-synchronous rectification
- Decoding logic for stepper motor full and half-step drive
- Cross conduction protection
- Thermal shutdown
- Undervoltage lockout
- Integrated fast free wheeling diodes



Description

The L6228Q is a DMOS fully integrated stepper motor driver, realized in BCD multipower technology.

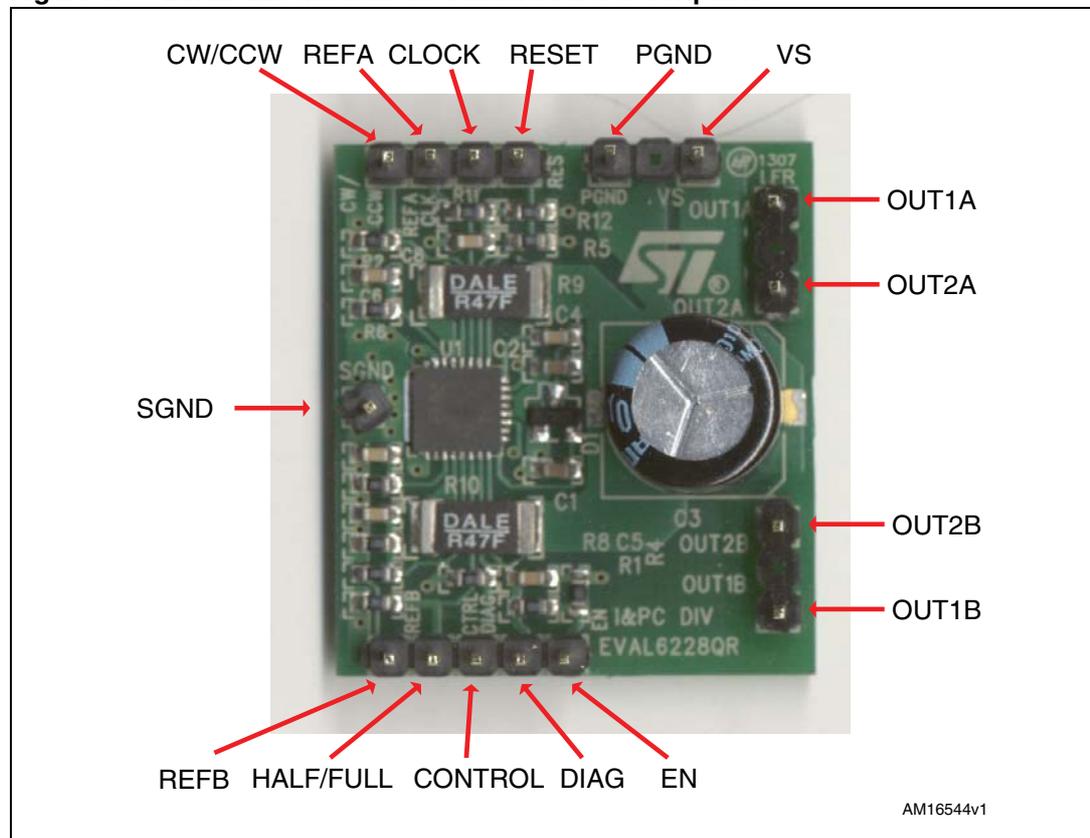
The L6228Q includes the circuitry to drive a two-phase bipolar stepper motor: a dual DMOS full-bridge, the constant off-time, the PWM current controller performing the chopping regulation and the phase sequence generator, which generates the stepping sequence. The device features a non-dissipative overcurrent protection on the high-side power MOSFETs and thermal shutdown.

1 Board description

Table 1. EVAL6228QR electrical specifications (recommended values)

Parameter	Value
Supply voltage range (VS)	8 V to 52 V DC
Output current rating (OUTx)	up to 1.4 A _{r.m.s.}
Switching frequency	up to 100 kHz
Voltage reference range (REFA, REFB)	0 to + 5 V
Input and enable voltage range	0 to + 5 V
L6228Q thermal resistance junction-to-ambient	42 °C/W

Figure 1. EVAL6228QR demonstration board description



AM16544v1

Table 2. EVAL6228QR pin connections

Name	Type	Function
VS	Power supply	Bridge A and bridge B power supply
PGND	Ground	Power ground terminal
CLOCK	Logic input	Step clock input
CW/CCW	Logic input	Selects the direction of the rotation (high = CW; low = CCW).
CONTROL	Logic input	Decay mode selector (high = SLOW decay; low = FAST decay)
HALF / FULL	Logic input	Step mode selector (high = half-step; low = full-step)
EN	Logic input / output	Chip-enable (active high). When low, all power DMOSs are switched OFF (both bridge A and bridge B).
RESET	Logic input	Reset pin (active low). When low, the phase sequence generator is reset to home state (state 1).
DIAG	Logic input	Diagnostic pin. When low, an overcurrent or overtemperature event is signaled.
SGND	Ground	Signal ground terminal
REFA	Analog input	Bridge A current controller reference voltage
REFB	Analog input	Bridge B current controller reference voltage
OUT1A	Power output	Bridge A output 1
OUT2A	Power output	Bridge A output 2
OUT1B	Power output	Bridge B output 1
OUT2B	Power output	Bridge B output 2

Figure 2. EVAL6228QR schematic

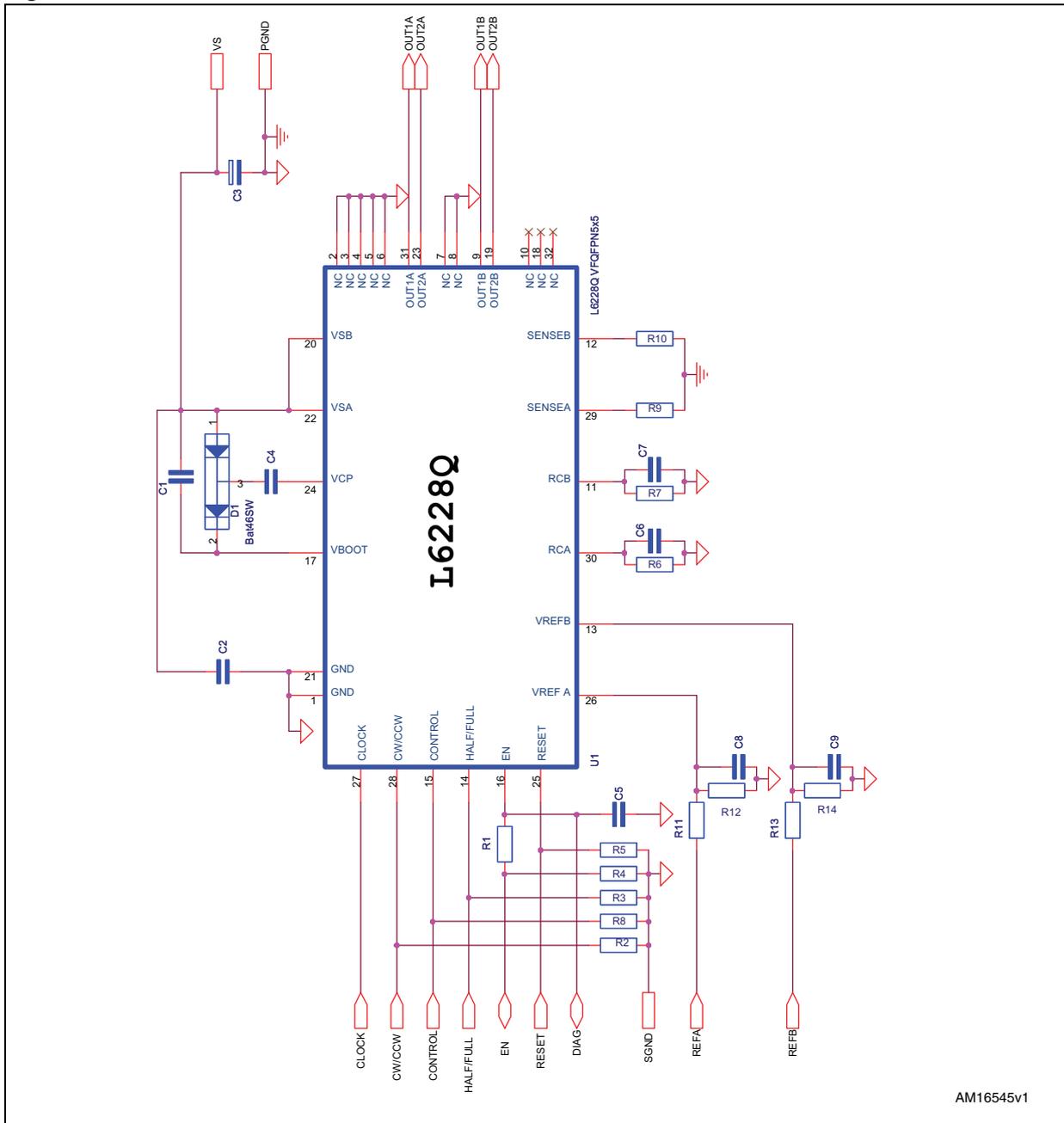


Table 3. EVAL6228QR part list

Part reference	Part value	Part description
C1	220 nF / 25 V	Capacitor
C2	220 nF / 63 V	Capacitor
C3	100 µF / 63 V	Capacitor
C4	10 nF / 25 V	Capacitor
C5	5.6 nF	Capacitor
C6, C7	820 pF	Capacitor
C8, C9	220 nF	Capacitor
D1	BAT46SW	Diode
R1, R2, R3, R4, R5, R8	100 kΩ 5% 0.25 W	Resistor
R6, R7	100 kΩ 1% 0.25 W	Resistor
R9, R10	0.4 Ω 1 W	Resistor
R11, R13	20 kΩ 1% 0.25 W	Resistor
R12, R14	2 kΩ 1% 0.25 W	Resistor
U1	L6228Q	Stepper motor driver in VFQFPN5x5

Figure 3. Component placement

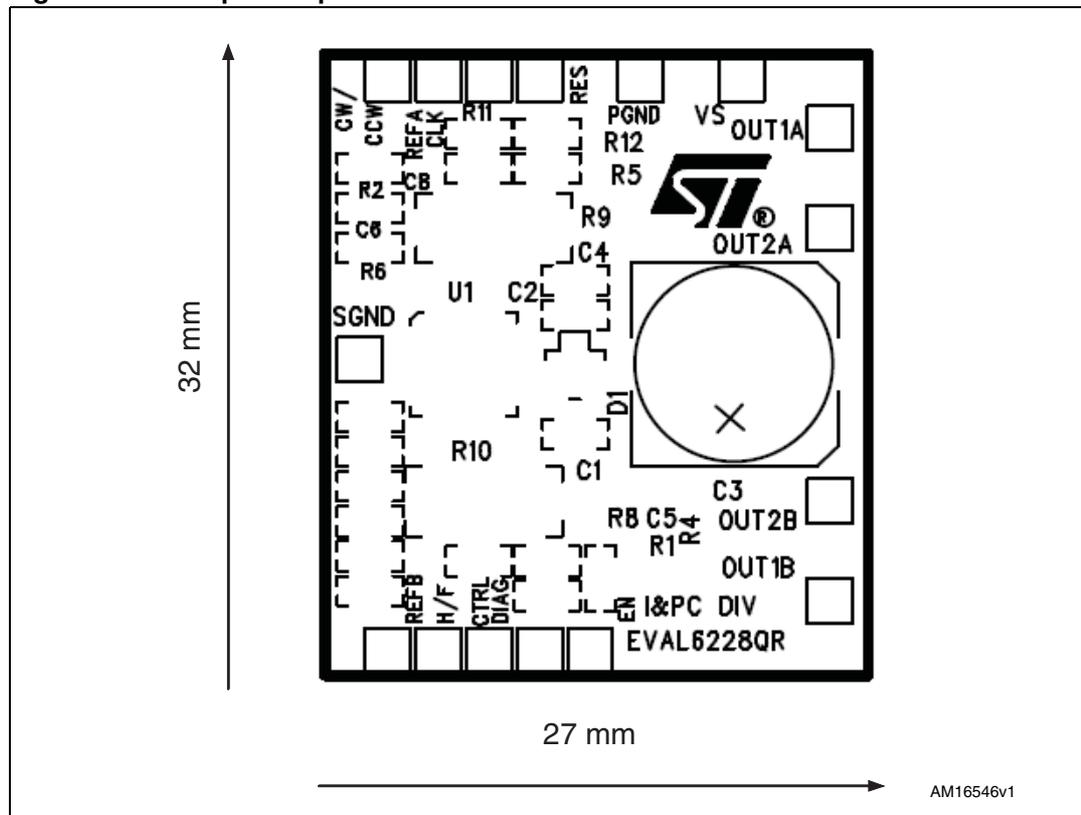


Figure 4. Top layer layout

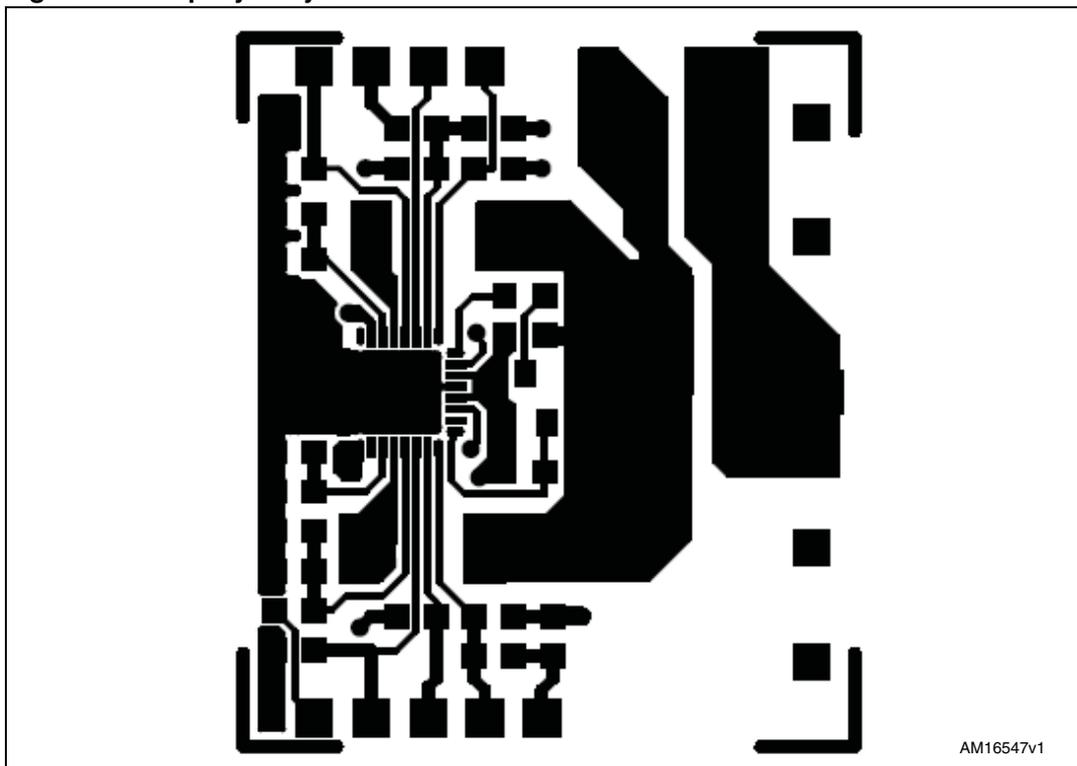
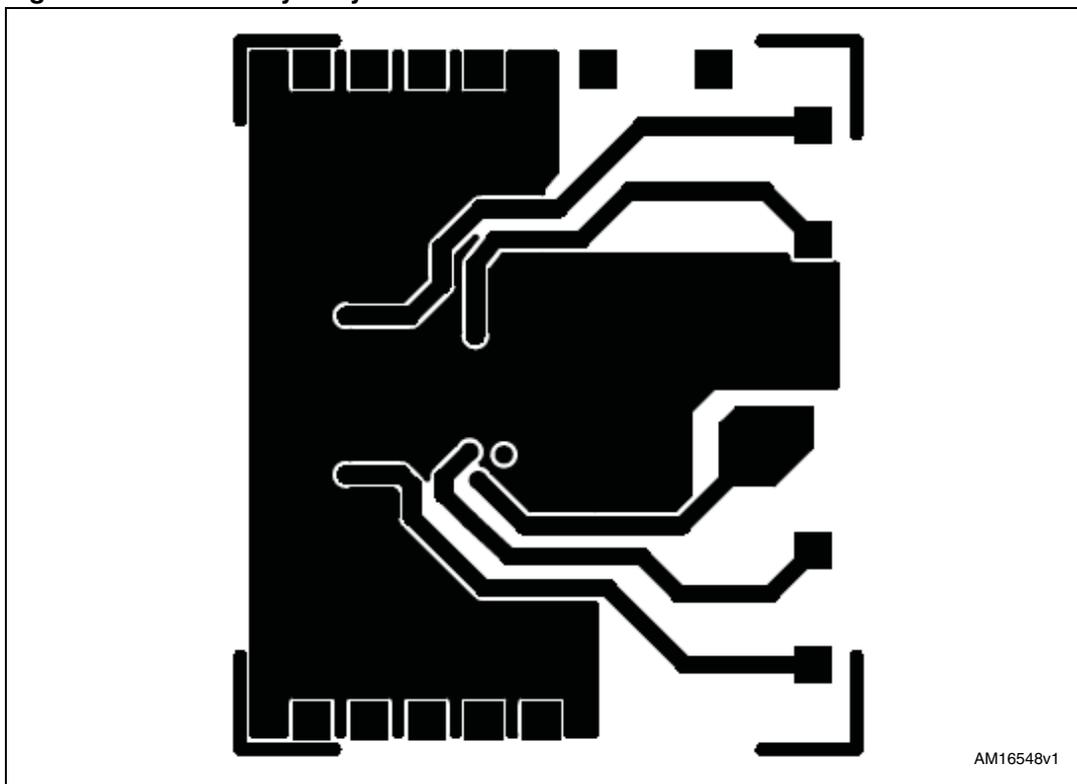


Figure 5. Bottom layer layout



2 Revision history

Table 4. Document revision history

Date	Revision	Changes
10-Jan-2013	1	Initial release.

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