## 915 MHz RF Powerharvester® Receiver



#### **DESCRIPTION**

The Powercast P2110B Powerharvester is an RF energy harvesting device that converts RF to DC. Housed in a compact SMD package, the P2110B receiver provides RF energy harvesting and power management for battery-free, micro-power devices. The P2110B converts RF energy to DC and stores it in a capacitor. When a charge threshold on the capacitor is achieved, the P2110B boosts the voltage to the set output voltage level and enables the voltage output. When the charge on the capacitor declines to the low voltage threshold the voltage output is turned off. A microprocessor can be used to optimize the power usage from the P2110B and obtain other data from the component for improving overall system operation.

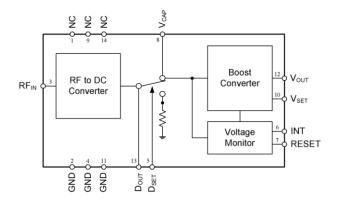
#### **FEATURES**

- High conversion efficiency
- Converts low-level RF signals enabling long range applications
- Regulated voltage output up to 5.5V
- Up to 50mA output current
- Received signal strength indicator
- No external RF components required Internally matched to 50 ohms
- Wide RF operating range
- Operation down to -12 dBm input power
- Externally resettable for microprocessor control
- Industrial temperature range
- RoHS compliant

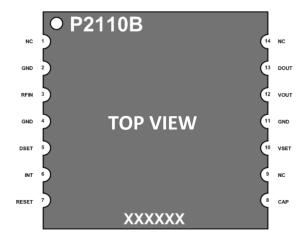
#### **APPLICATIONS**

- Battery-free wireless sensors
  - Industrial Monitoring
  - Smart Grid
  - Defense
  - Building automation
  - Oil & Gas
- Battery recharging
  - -Coin cells
  - -Thin-film cells
- Low power electronics

#### **FUNCTIONAL BLOCK DIAGRAM**



#### **PIN CONFIGURATION**



Powercast products and technology are covered by one or more of the following patents and other patents pending: 6,289,237 | 6,615,074 | 6,856,291 | 7,027,311 | 7,057,514 | 7,639,994 | 7,643,312 | 7,812,771 | 7,844,306 | 7,868,482 | 7,898,105 | 7,925,308 | 8,159,090 | 8,380,255 | 8,432,062 | 8,461,817 | 8,621,245

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#### **ABSOLUTE MAXIMUM RATINGS**

 $T_A = 25$ °C, unless otherwise noted.

Parameter	Rating	Unit
RF Input Power	23	dBm
RF <sub>IN</sub> to GND	0	V
D <sub>SET</sub> to GND	6	V
RESET to GND	6	V
V <sub>CAP</sub> to GND	2.3	V
V <sub>OUT</sub> to GND	6	V
Vout Current	100	mA
Operating Temperature Range	-40 to 85	°C
Storage Temperature Range	-40 to 140	°C

Exceeding the absolute maximum ratings may cause permanent damage to the device.

#### **ESD CAUTION**

This is an ESD (electrostatic discharge) sensitive device. Proper ESD precautions should be taken to avoid degradation or damage to the component.



#### PIN FUNCTIONAL DESCRIPTIONS

Pin	Label	Function	
1	NC	No Connection.	
2	GND	RF Ground. Connect to analog ground plane.	
3	RF <sub>IN</sub>	RF Input. Connect to $50\Omega$ antenna through a $50\Omega$ transmission line. Add	
		a DC block if antenna is a DC short.	
4	GND	RF Ground. Connect to analog ground plane.	
5	$D_SET$	Digital Input. Set to enable measurement of harvested power. If this	
		function is not desired leave NC.	
6	INT	Digital Output. Indicates that voltage is present at V <sub>OUT</sub> .	
7	RESET	Digital Input. Set to disable V <sub>OUT</sub> . If this function is not desired leave NC.	
8	$V_{CAP}$	Connect to an external capacitor for energy storage.	
9	NC	No Connection.	
10	$V_{SET}$	Output Voltage Adjustment. Sets the output voltage by connecting a	
		resistor to V <sub>OUT</sub> or GND. Leave NC for 3.3V.	
11	GND	DC Ground. Connect to analog ground plane.	
12	$V_{OUT}$	DC Output. Connect to external device. The output is preset to 3.3V but	
		can be adjusted with an external resistor.	
13	D <sub>out</sub>	Analog Output. Provides an analog voltage level corresponding to the	
		harvested power.	
14	NC	No Connection.	

## 915 MHz RF Powerharvester® Receiver



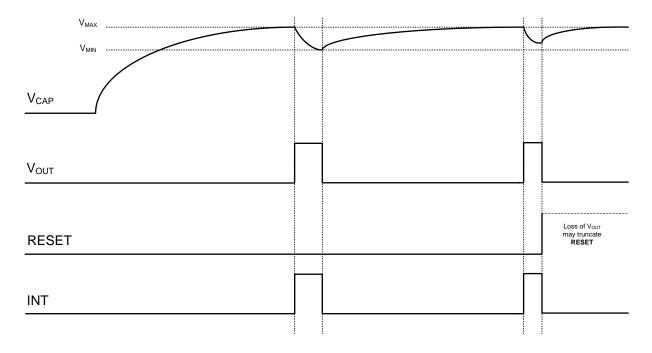
### **SPECIFICATIONS**

 $T_A = 25$ °C, RF<sub>IN</sub> = 915MHz unless otherwise noted.

Parameter	Symbol	Condition	Min	Тур	Max	Unit
RF Characteristics <sup>1</sup>						
Input Power	RF <sub>IN</sub>		-12		10	dBm
Frequency			902		928	MHz
DC Characteristics						
Output Voltage	$V_{OUT}$		2.0	3.3	5.5	V
Output Current	Іоит				50	mA
V <sub>CAP</sub> Maximum	$V_{MAX}$			1.25		V
V <sub>CAP</sub> Minimum	$V_{MIN}$			1.02		V
Signal Strength	D <sub>out</sub>	RFIN = OdBm		275		mV
Boost Efficiency		Iоит = <b>20mA</b>		85		%
Maximum INT Current				0.1		mA
Digital Characteristics						
RESET Input High				1		V
Dset Input High			1.8			V
INT Output High			VMIN		VMAX	V
Timing Characteristics						
D <sub>SET</sub> Delay				50		μs
RESET Delay				6.6		μs
RESET Pulse Width			20			ns

<sup>&</sup>lt;sup>1</sup>See typical performance graphs for operation at other frequencies or power levels.

## **TIMING DIAGRAM**



## 915 MHz RF Powerharvester® Receiver



#### **FUNCTIONAL DESCRIPTION**

#### RF INPUT (RF<sub>IN</sub>)

The RF input is an unbalanced input from the antenna. Any standard or custom  $50\Omega$  antenna may be used with the receiver. The P2110B has been optimized for operation in the 902-928MHz band but will operate outside this band with reduced efficiency. Contact Powercast for custom frequency requirements.

The RF input must be isolated from ground. For antennas that are a DC short, a high-Q DC blocking capacitor should be added in series with the antenna.

#### STORAGE CAPACITOR SELECTION (V<sub>CAP</sub>)

The P2110B requires an external storage capacitor connected at  $V_{CAP}$ . The value of the capacitor will determine the amount of energy available from the  $V_{OUT}$  pin. The capacitor should have a leakage current as small as possible. It is recommended that the leakage current of the capacitor be less than  $1\mu A$  at 1.2V. The capacitor ESR should be  $200m\Omega$  or less.

Smaller capacitors will charge more quickly but will result in shorter operation cycles. Larger capacitors will charge more slowly, but will provide for longer operation cycles. The minimum required capacitor value can be estimated using the following equation.

 $C = 15V_{OUT}I_{OUT}t_{ON}$ 

Where,

 $V_{OUT}$  - Output voltage

 $I_{OUT}$  - Average output current

 $t_{ON}$  - On-time of the output voltage

When using the RESET function, the size of the capacitor is less important. A larger capacitor can be used to facilitate intermittent functions that require more energy. The RESET will control the amount of energy removed from the capacitor during operation which will minimize the required recharge time. It should be noted that when RESET is used, a larger capacitor will not affect charge time during operation, but it will require more time to initially charge from a completely discharged state.

The voltage on the VCAP pin under normal operation will vary between approximately 1.25V and 1.02V. If the harvested energy becomes too large, the voltage on the capacitor will be internally clamped to protect low voltage supercapacitors. Clamping will begin at approximately 1.8V and will limit the voltage to less than 2.3V at the maximum rated input power.

## RSSI OPERATION (Dout, Dset)

The RSSI functionality allows the sampling of the received signal to provide an indication of the amount of energy being harvested. When  $D_{SET}$  is driven high the harvested DC power will be directed to an internal sense resistor, and the corresponding voltage will be provided to the  $D_{OUT\ pin}$ . The voltage on the  $D_{OUT\ pin}$  can be read after a 50µs settling time.

When the RSSI functionality is being used, the harvested DC power is not being stored.

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If the RSSI functionality is not used,  $D_{OUT}$  and  $D_{SET}$  should be left unconnected.  $D_{SET}$  is internally pulled down.

#### DATA RETRIEVAL (D<sub>OUT</sub>, D<sub>SET</sub>)

Using D<sub>OUT</sub> and D<sub>SET</sub> it is possible to collect data from the RF transmitter that is supplying power to the P2110B. As discussed above, with D<sub>SET</sub> high, D<sub>OUT</sub> will provide a voltage across R3 that can be read by an ADC. However, the voltage on D<sub>OUT</sub> will also follow the power level of the RF field as the power level changes. If the RF field is being provided by a transmitter that is also communicating by modulating its amplitude, such as the Powercast TX91501-3W-ID Powercaster® transmitter, the data can be read by the P2110B. The voltage level will need gained up using operation amplifiers and supplied to a device that can read the data pattern supplied by the transmitter.

#### RESET

The RESET function allows the voltage from Vout to be turned off before the storage capacitor reaches the lower threshold, VMIN, thereby saving energy and improving the recharge time back to the activation threshold, VMAX. The RESET function can be implemented by a microcontroller. When the function of the microcontroller is completed, driving RESET high will disable the voltage from Vout. Care should be taken that the microcontroller, ensure especially during power-on, does not inadvertently drive RESET high. This will immediately shutdown the output voltage.

If the RESET functionality is not used, RESET should be left unconnected. RESET in internally pulled down

#### INTERRUPT (INT)

The interrupt function provides a digital indication that voltage (or current) is present at the V<sub>OUT</sub> pin. INT can be used in more sophisticated systems that contain other storage elements and can be used as an external interrupt to bring a device such as microcontroller out of a deep sleep mode. The digital high level of the INT pin will be between V<sub>MIN</sub> and V<sub>MAX</sub>. The INT pin can provide a maximum of 0.1mA of current.

If the INT functionality is not used, INT should be left unconnected.

#### **SETTING THE OUTPUT VOLTAGE (VOUT)**

The DC output voltage from the P2110 is preset to 3.3V. However, it can be adjusted by adding an external resistor to increase or decrease the output voltage using the following equations.  $(M=10^6)$ 

To **decrease** the output voltage, place a resistor calculated by the following equation from VSET to VOUT. The voltage can be set to a minimum of 2.0V.

$$R = \frac{1M (V_{OUT} - 1.21)}{(3.32 - V_{OUT})}$$

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To **increase** the output voltage, place a resistor calculated by the following equation from VSET to GND. The voltage can be set to a maximum of 5.5V.

$$R = \frac{1.21M}{(V_{OUT} - 3.32)}$$

#### LAYOUT CONSIDERATIONS

The RF<sub>IN</sub> feed line should be designed as a  $50\Omega$  trace and should be as short as possible to minimize feed line losses. The following table provides recommended dimensions for  $50\Omega$  feed lines (CPWG) for different circuit board configurations.

PCB Side View			
I	GND→ W	w GND	_
	$\epsilon_{r}$	Ĥ	=
	GND		
Material	Thickness	Trace Width	Spacing
	(H)	(S)	(W)
FR4	62	50	9
$(\varepsilon_r = 4.2)$			
FR4	31	50	20
$(\varepsilon_r = 4.2)$			

<sup>\*</sup>All dimensions are in mils.

The GND pins on each side of the RFIN pin should be connected to the PCB ground plane through a via located next to the pads under the receiver.

When setting the output voltage, the resistor connected to the VSET pin should be as close as possible to the pin. No external capacitance should be added to this pin.

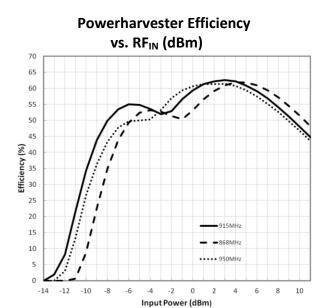
The  $D_{OUT}$  pin can contain low-level analog voltage signals. If a long trace is connected to this pin, additional filtering capacitance next to the A/D converter may be required. Additional capacitance on this pin will increase the  $D_{SET}$  delay time.

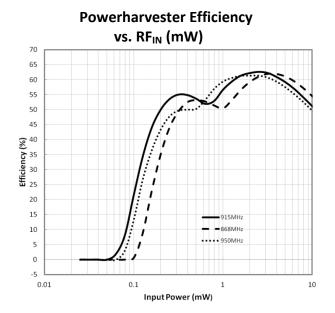
The trace from VCAP to the storage capacitor should be as short as possible and have a width of greater than 20mils to minimize the series resistance of the trace.



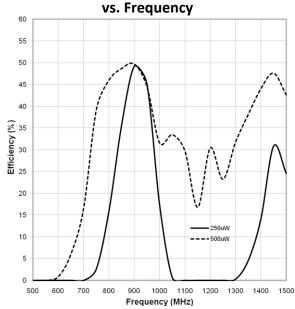
#### **TYPICAL PERFORMANCE GRAPHS**

 $T_A$  = 25°C,  $V_{OUT}$  = 3.3V,  $V_{CAP}$  = 1.2V, unless otherwise noted

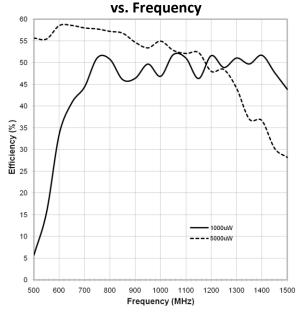




## Powerharvester Efficiency



## **Powerharvester Efficiency**

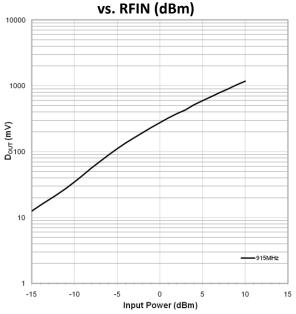




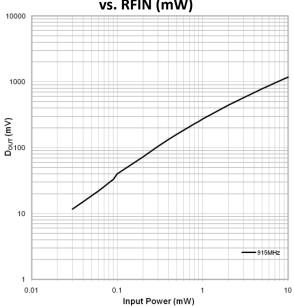
#### **TYPICAL PERFORMANCE GRAPHS**

 $T_A = 25$ °C,  $V_{OUT} = 3.3V$ ,  $V_{CAP} = 1.2V$ , unless otherwise noted

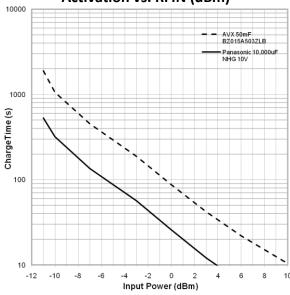
# Received Signal Strength Indicator vs. RFIN (dBm)



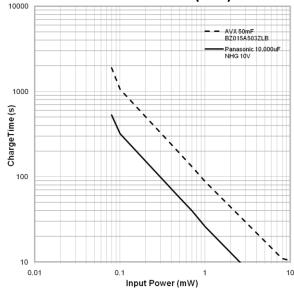
# Received Signal Strength Indicator vs. RFIN (mW)



# Initial CAP Charge Time to First Activation vs. RFIN (dBm)



# Initial CAP Charge Time to First Activation vs. RFIN (mW)



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## **P2110 MODULE SERIES**

MODULE	STATUS	FUNCTIONAL DIFFERENCES
P2110	End-of-life	N/A
P2110B	Replaced P2110	Increased RF-to-DC conversion efficiency
		<ul> <li>V<sub>SET</sub> resistor values modified</li> </ul>

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## Contact Us:

## > Address:

401 Building No.5, JiuGe Business Center, Lane 2301, Yishan Rd Minhang District, Shanghai , China

## > Sales:

Direct +86 (21) 6401-6692

Email amall@ameya360.com

QQ 800077892

Skype ameyasales1 ameyasales2

## Customer Service :

Email service@ameya360.com

## Partnership :

Tel +86 (21) 64016692-8333

Email mkt@ameya360.com