

FSB50550T

Motion SPM® 5 Series

Features

- UL Certified No. E209204 (UL1557)
- 500 V $R_{DS(on)} = 1.7 \Omega$ (Max) FRFET MOSFET 3-Phase Inverter with Gate Drivers
- Separate Open-Source Pins from Low-Side MOSFETs for Three-Phase Current-Sensing
- Active-HIGH Interface, Works with 3.3 / 5 V Logic, Schmitt-trigger Input
- Optimized for Low Electromagnetic Interference
- HVIC for Gate Driving and Under-Voltage Protection
- Isolation Rating: 1500 V_{rms} / min.
- RoHS Compliant

Applications

- 3-Phase Inverter Driver for Small Power AC Motor Drives

Related Source

- [AN-9082 - Motion SPM5 Series Thermal Performance by Contact Pressure](#)
- [AN-9080 - User's Guide for Motion SPM 5 Series Ver.1](#)

General Description

The FSB50550T is an advanced Motion SPM® 5 module providing a fully-featured, high-performance inverter output stage for AC Induction, BLDC and PMSM motors. These modules integrate optimized gate drive of the built-in MOSFETs (FRFET® technology) to minimize EMI and losses. The built-in, high-speed HVIC requires only a single supply voltage and translates the incoming logic-level gate inputs to the high-voltage, high-current drive signals required to properly drive the module's internal MOSFETs. Separate open-source MOSFET terminals are available for each phase to support the widest variety of control algorithms.



Package Marking & Ordering Information

Device	Device Marking	Package	Packing Type	Quantity
FSB50550T	FSB50550T	SPM5F-023	Rail	15

Absolute Maximum Ratings

Inverter Part (each MOSFET unless otherwise specified.)

Symbol	Parameter	Conditions	Rating	Unit
V_{DSS}	Drain-Source Voltage of Each MOSFET		500	V
$*I_{D\ 25}$	Each MOSFET Drain Current, Continuous	$T_C = 25^\circ\text{C}$	1.8	A
$*I_{D\ 80}$	Each MOSFET Drain Current, Continuous	$T_C = 80^\circ\text{C}$	1.2	A
$*I_{DP}$	Each MOSFET Drain Current, Peak	$T_C = 25^\circ\text{C}$, $PW < 100\ \mu\text{s}$	3.5	A
$*P_D$	Maximum Power Dissipation	$T_C = 25^\circ\text{C}$, For Each MOSFET	4.5	W

Control Part (each HVIC unless otherwise specified.)

Symbol	Parameter	Conditions	Rating	Unit
V_{CC}	Control Supply Voltage	Applied Between V_{CC} and COM	20	V
V_{BS}	High-side Bias Voltage	Applied Between V_B and V_S	20	V
V_{IN}	Input Signal Voltage	Applied Between IN and COM	$-0.3 \sim V_{CC} + 0.3$	V

Thermal Resistance

Symbol	Parameter	Conditions	Rating	Unit
$R_{\theta JC}$	Junction to Case Thermal Resistance	Each MOSFET under Inverter Operating Condition (1st Note 1)	8.6	$^\circ\text{C}/\text{W}$

Total System

Symbol	Parameter	Conditions	Rating	Unit
T_J	Operating Junction Temperature		$-20 \sim 150$	$^\circ\text{C}$
T_{STG}	Storage Temperature		$-50 \sim 150$	$^\circ\text{C}$
V_{ISO}	Isolation Voltage	60 Hz, Sinusoidal, 1 Minute, Connect Pins to Heat Sink Plate	1500	V_{rms}

1st Notes:

- For the measurement point of case temperature T_C , please refer to Figure 4.
- Marking " * " is calculation value or design factor.

Pin descriptions

Pin Number	Pin Name	Pin Description
1	COM	IC Common Supply Ground
2	$V_{B(U)}$	Bias Voltage for U Phase High Side MOSFET® Driving
3	$V_{CC(U)}$	Bias Voltage for U Phase IC and Low Side MOSFET Driving
4	$IN_{(UH)}$	Signal Input for U Phase High-Side
5	$IN_{(UL)}$	Signal Input for U Phase Low-Side
6	N.C	No Connection
7	$V_{B(V)}$	Bias Voltage for V Phase High Side MOSFET Driving
8	$V_{CC(V)}$	Bias Voltage for V Phase IC and Low Side MOSFET Driving
9	$IN_{(VH)}$	Signal Input for V Phase High-Side
10	$IN_{(VL)}$	Signal Input for V Phase Low-Side
11	N.C	No Connection
12	$V_{B(W)}$	Bias Voltage for W Phase High Side MOSFET Driving
13	$V_{CC(W)}$	Bias Voltage for W Phase IC and Low Side MOSFET Driving
14	$IN_{(WH)}$	Signal Input for W Phase High-Side
15	$IN_{(WL)}$	Signal Input for W Phase Low-Side
16	N.C	No Connection
17	P	Positive DC-Link Input
18	U, $V_{S(U)}$	Output for U Phase & Bias Voltage Ground for High Side MOSFET Driving
19	N_U	Negative DC-Link Input for U Phase
20	N_V	Negative DC-Link Input for V Phase
21	V, $V_{S(V)}$	Output for V Phase & Bias Voltage Ground for High Side MOSFET Driving
22	N_W	Negative DC-Link Input for W Phase
23	W, $V_{S(W)}$	Output for W Phase & Bias Voltage Ground for High Side MOSFET Driving

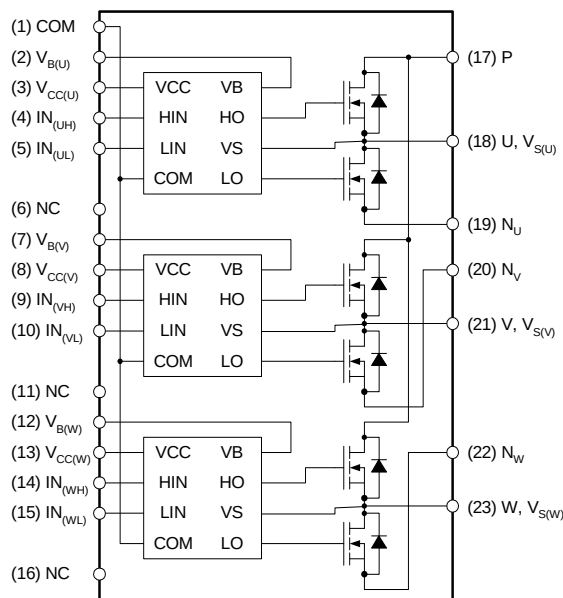


Figure 1. Pin Configuration and Internal Block Diagram (Bottom View)

1st Notes:

- Source terminal of each low-side MOSFET is not connected to supply ground or bias voltage ground inside Motion SPM® 5 product. External connections should be made as indicated in Figure 3.

Electrical Characteristics ($T_J = 25^\circ\text{C}$, $V_{CC} = V_{BS} = 15\text{ V}$ unless otherwise specified.)**Inverter Part** (each MOSFET unless otherwise specified.)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
BV_{DSS}	Drain - Source Breakdown Voltage	$V_{IN} = 0\text{ V}$, $I_D = 250\text{ }\mu\text{A}$ (2nd Note 1)	500	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{IN} = 0\text{ V}$, $V_{DS} = 500\text{ V}$	-	-	250	μA
$R_{DS(on)}$	Static Drain - Source Turn-On Resistance	$V_{CC} = V_{BS} = 15\text{ V}$, $V_{IN} = 5\text{ V}$, $I_D = 1.2\text{ A}$	-	1.3	1.7	Ω
V_{SD}	Drain - Source Diode Forward Voltage	$V_{CC} = V_{BS} = 15\text{ V}$, $V_{IN} = 0\text{ V}$, $I_D = -1.2\text{ A}$	-	-	1.2	V
t_{ON}	Switching Times	$V_{PN} = 300\text{ V}$, $V_{CC} = V_{BS} = 15\text{ V}$, $I_D = 1.2\text{ A}$ $V_{IN} = 0\text{ V} \leftrightarrow 5\text{ V}$, Inductive Load $L = 3\text{ mH}$ High- and Low-Side MOSFET Switching (2nd Note 2)	-	560	-	ns
t_{OFF}			-	440	-	ns
t_{rr}			-	130	-	ns
E_{ON}			-	71	-	μJ
E_{OFF}			-	11	-	μJ
RBSOA	Reverse Bias Safe Operating Area	$V_{PN} = 400\text{ V}$, $V_{CC} = V_{BS} = 15\text{ V}$, $I_D = I_{DP}$, $V_{DS} = BV_{DSS}$, $T_J = 150^\circ\text{C}$ High- and Low-Side MOSFET Switching (2nd Note 3)	Full Square			

Control Part (each HVIC unless otherwise specified.)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
I_{QCC}	Quiescent V_{CC} Current	$V_{CC} = 15\text{ V}$, $V_{IN} = 0\text{ V}$ Applied Between V_{CC} and COM	-	-	160	μA
I_{QBS}	Quiescent V_{BS} Current	$V_{BS} = 15\text{ V}$, $V_{IN} = 0\text{ V}$ Applied Between $V_{B(U)} - U$, $V_{B(V)} - V$, $V_{B(W)} - W$	-	-	100	μA
UV_{CCD}	Low-Side Under-Voltage Protection (Figure 8)	V_{CC} Under-Voltage Protection Detection Level	7.4	8.0	9.4	V
UV_{CCR}		V_{CC} Under-Voltage Protection Reset Level	8.0	8.9	9.8	V
UV_{BSD}	High-Side Under-Voltage Protection (Figure 9)	V_{BS} Under-Voltage Protection Detection Level	7.4	8.0	9.4	V
UV_{BSR}		V_{BS} Under-Voltage Protection Reset Level	8.0	8.9	9.8	V
V_{IH}	ON Threshold Voltage	Logic HIGH Level	2.9	-	-	V
V_{IL}	OFF Threshold Voltage	Logic LOW Level	-	-	0.8	V
I_{IH}	Input Bias Current	$V_{IN} = 5\text{ V}$	-	10	20	μA
I_{IL}		$V_{IN} = 0\text{ V}$	-	-	2	μA

2nd Notes:

- BV_{DSS} is the absolute maximum voltage rating between drain and source terminal of each MOSFET inside Motion SPM® 5 product. V_{PN} should be sufficiently less than this value considering the effect of the stray inductance so that V_{PN} should not exceed BV_{DSS} in any case.
- t_{ON} and t_{OFF} include the propagation delay of the internal drive IC. Listed values are measured at the laboratory test condition, and they can be different according to the field applications due to the effect of different printed circuit boards and wirings. Please see Figure 4 for the switching time definition with the switching test circuit of Figure 5.
- The peak current and voltage of each MOSFET during the switching operation should be included in the Safe Operating Area (SOA). Please see Figure 5 for the RBSOA test circuit that is same as the switching test circuit.

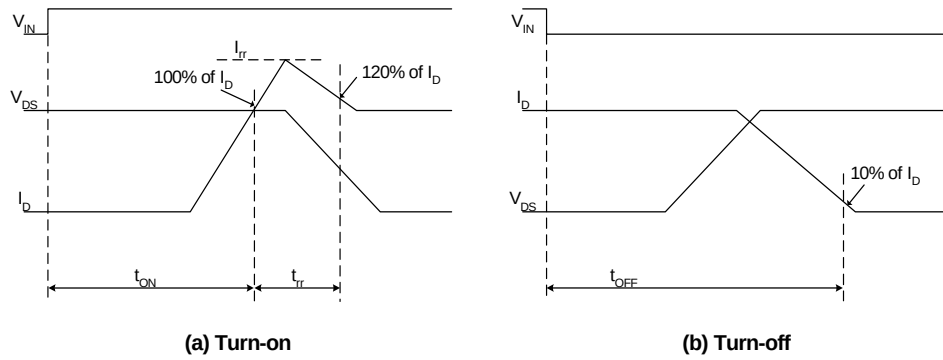


Figure 4. Switching Time Definitions

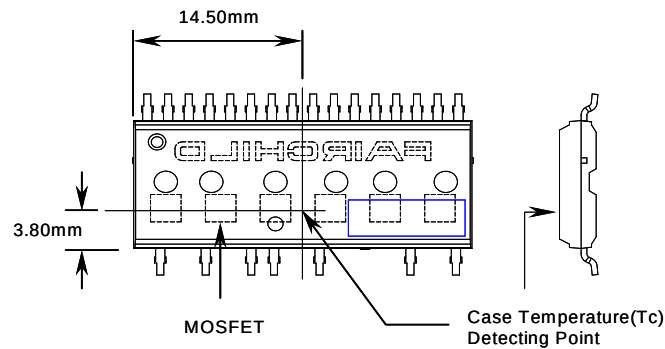


Figure 5. Switching and RBSOA (Single-pulse) Test Circuit (Low-side)

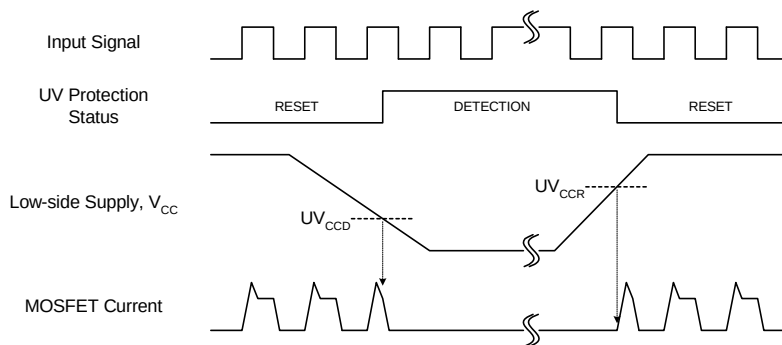


Figure 6. Under-Voltage Protection (Low-Side)

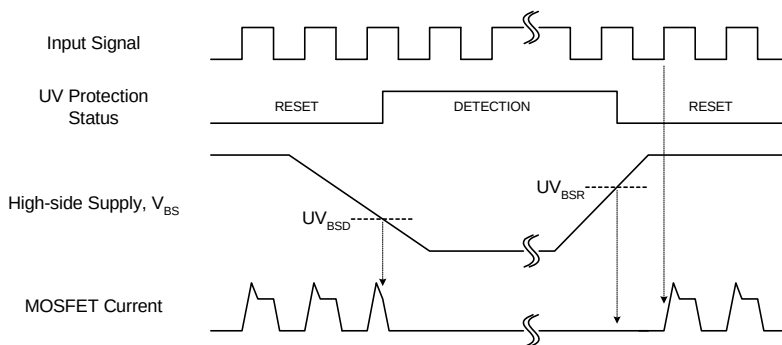
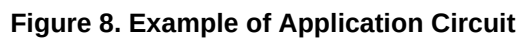


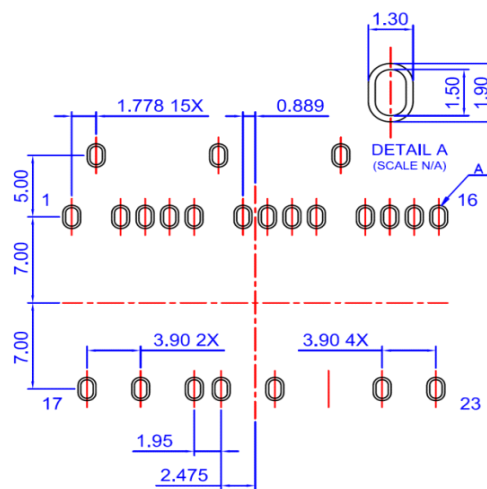
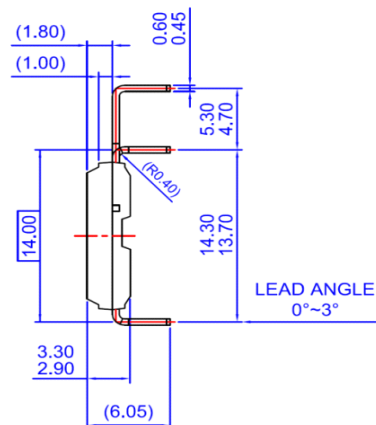
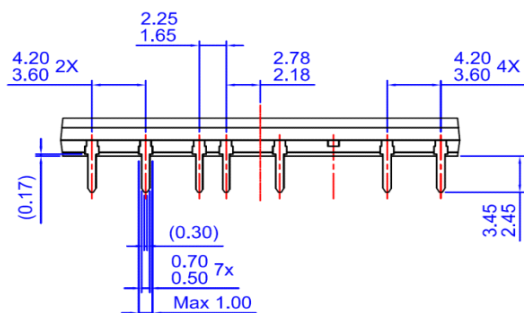
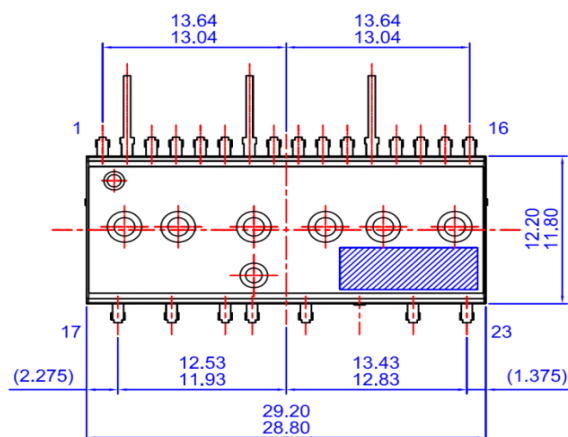
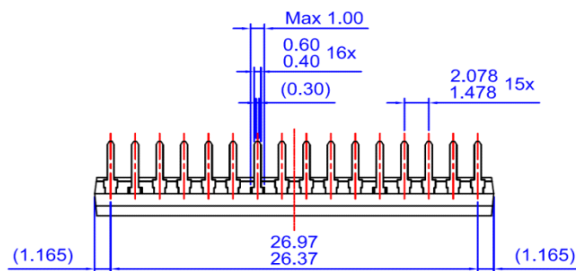
Figure 7. Under-Voltage Protection (High-Side)



1. About pin position, refer to Figure 1.

2. RC-coupling (R_5 and C_5 , R_4 and C_6) and C_4 at each input of Motion SPM[®] 5 product and MCU are useful to prevent improper input signal caused by surge-noise.
3. The voltage-drop across R_3 affects the low-side switching performance and the bootstrap characteristics since it is placed between COM and the source terminal of the low-side MOSFET. For this reason, the voltage-drop across R_3 should be less than 1 V in the steady-state.
4. Ground-wires and output terminals, should be thick and short in order to avoid surge-voltage and malfunction of HVIC.
5. All the filter capacitors should be connected close to Motion SPM 5 product, and they should have good characteristics for rejecting high-frequency ripple current.

Detailed Package Outline Drawings



- NOTES: UNLESS OTHERWISE SPECIFIED
 A) THIS PACKAGE DOES NOT COMPLY TO ANY CURRENT PACKAGING STANDARD
 B) ALL DIMENSIONS ARE IN MILLIMETERS
 C) DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS
 D) () IS REFERENCE
 E) [] IS ASS'Y QUALITY
 F) DRAWING FILENAME: MOD23DFREV2.0
 G) FAIRCHILD SEMICONDUCTOR

LAND PATTERN RECOMMENDATIONS

Package drawings are provided as a service to customers considering Fairchild components. Drawings may change in any manner without notice. Please note the revision and/or data on the drawing and contact a FairchildSemiconductor representative to verify or obtain the most recent revision. Package specifications do not expand the terms of Fairchild's worldwide terms and conditions, specifically the warranty therein, which covers Fairchild products.

Always visit Fairchild Semiconductor's online packaging area for the most recent package drawings:

<http://www.fairchildsemi.com/dwg/MO/MOD23DF.pdf>



TRADEMARKS

The following includes registered and unregistered trademarks and service marks, owned by Fairchild Semiconductor and/or its global subsidiaries, and is not intended to be an exhaustive list of all such trademarks.

AccuPower™	F-PFS™	PowerTrench®	Sync-Lock™
AX-CAP®*	FRFET™	PowerXS™	SYSTEM GENERAL®
BitSiC™	Global Power Resource™	Programmable Active Droop™	TinyBoost®
Build it Now™	GreenBridge™	QFET®	TinyBuck®
CorePLUS™	Green FPS™	QS™	TinyCalc™
CorePOWER™	Green FPS™ e-Series™	Quiet Series™	TinyLogic®
CROSSVOLT™	Gmax™	RapidConfigure™	TINYOPTO™
CTL™	GTO™	Saving our world, 1mW/W/kW at a time™	TinyPower™
Current Transfer Logic™	IntelliMAX™	SignalWise™	TinyPulse™
DEUXPEED®	ISOPLANAR™	SmartMax™	TinyWire™
Dual Cool™	Making Small Speakers Sound Louder and Better™	SMART START™	TranSiC™
EcoSPARK®	MegaBuck™	Solutions for Your Success™	TriFault Detect™
EfficientMax™	MICROCOUPLER™	SPM®	TRUECURRENT®*
ESBC™	MicroFET™	STEALTH™	µSerDes™
F [®]	MicroPak™	SuperFET®	UHC®
Fairchild®	MicroPak2™	SuperSOT™-3	Ultra FRFET™
Fairchild Semiconductor®	MillerDrive™	SuperSOT™-6	UniFET™
FACT Quiet Series™	MotionMax™	SuperSOT™-8	VCX™
FACT®	mWSaver®	SupreMOS®	VisualMax™
FAST®	OptoHiT™	SyncFET™	VoltagePlus™
FastvCore™	OPTOLOGIC®		XS™
FETBench™	OPTOPLANAR®		
FPS™			

* Trademarks of System General Corporation, used under license by Fairchild Semiconductor.

DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION, OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS. THESE SPECIFICATIONS DO NOT EXPAND THE TERMS OF FAIRCHILD'S WORLDWIDE TERMS AND CONDITIONS, SPECIFICALLY THE WARRANTY THEREIN, WHICH COVERS THESE PRODUCTS.

LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION.

As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body or (b) support or sustain life, and (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury of the user.
2. A critical component in any component of a life support, device, or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

ANTI-COUNTERFEITING POLICY

Fairchild Semiconductor Corporation's Anti-Counterfeiting Policy. Fairchild's Anti-Counterfeiting Policy is also stated on our external website, www.fairchildsemi.com, under Sales Support.

Counterfeiting of semiconductor parts is a growing problem in the industry. All manufacturers of semiconductor products are experiencing counterfeiting of their parts. Customers who inadvertently purchase counterfeit parts experience many problems such as loss of brand reputation, substandard performance, failed applications, and increased cost of production and manufacturing delays. Fairchild is taking strong measures to protect ourselves and our customers from the proliferation of counterfeit parts. Fairchild strongly encourages customers to purchase Fairchild parts either directly from Fairchild or from Authorized Fairchild Distributors who are listed by country on our web page cited above. Products customers buy either from Fairchild directly or from Authorized Fairchild Distributors are genuine parts, have full traceability, meet Fairchild's quality standards for handling and storage and provide access to Fairchild's full range of up-to-date technical and product information. Fairchild and our Authorized Distributors will stand behind all warranties and will appropriately address any warranty issues that may arise. Fairchild will not provide any warranty coverage or other assistance for parts bought from Unauthorized Sources. Fairchild is committed to combat this global problem and encourage our customers to do their part in stopping this practice by buying direct or from authorized distributors.

PRODUCT STATUS DEFINITIONS

Definition of Terms

Datasheet Identification	Product Status	Definition
Advance Information	Formative / In Design	Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
Obsolete	Not In Production	Datasheet contains specifications on a product that is discontinued by Fairchild Semiconductor. The datasheet is for reference information only.

Rev. I66

AMEYA360

Components Supply Platform

Authorized Distribution Brand :



Website :

Welcome to visit www.ameya360.com

Contact Us :

➤ Address :

401 Building No.5, JiuGe Business Center, Lane 2301, Yishan Rd
Minhang District, Shanghai , China

➤ Sales :

Direct +86 (21) 6401-6692

Email amall@ameya360.com

QQ 800077892

Skype ameyasales1 ameyasales2

➤ Customer Service :

Email service@ameya360.com

➤ Partnership :

Tel +86 (21) 64016692-8333

Email mkt@ameya360.com