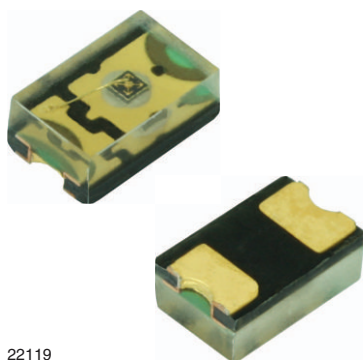


## High Speed Infrared Emitting Diodes, 850 nm, Surface Emitter Technology



22119

### DESCRIPTION

As part of the [SurfLight™](#) portfolio, the VSMY1850 is an infrared, 850 nm emitting diode based on GaAlAs surface emitter chip technology with high radiant intensity, high optical power and high speed, molded in clear, untinted 0805 plastic package for surface mounting (SMD).

### FEATURES

- Package type: surface mount
- Package form: 0805
- Dimensions (L x W x H in mm): 2 x 1.25 x 0.85
- Peak wavelength:  $\lambda_p = 850$  nm
- High reliability
- High radiant power
- High radiant intensity
- High speed
- Angle of half sensitivity:  $\phi = \pm 60^\circ$
- Suitable for high pulse current operation
- 0805 standard surface-mountable package
- Floor life: 168 h, MSL 3, acc. J-STD-020
- Lead (Pb)-free reflow soldering
- Material categorization: for definitions of compliance please see [www.vishay.com/doc?99912](http://www.vishay.com/doc?99912)



**RoHS**  
COMPLIANT  
HALOGEN  
**FREE**  
**GREEN**  
(5-2008)

### APPLICATIONS

- IrDA compatible data transmission
- Miniature light barrier
- Photointerrupters
- Optical switch
- Emitter source for proximity sensors
- IR touch panels
- IR Flash
- IR illumination
- 3D TV

### PRODUCT SUMMARY

COMPONENT	$I_e$ (mW/sr)	$\phi$ (deg)	$\lambda_p$ (nm)	$t_r$ (ns)
VSMY1850	10	$\pm 60$	850	10

#### Note

- Test conditions see table "Basic Characteristics"

### ORDERING INFORMATION

ORDERING CODE	PACKAGING	REMARKS	PACKAGE FORM
VSMY1850	Tape and reel	MOQ: 3000 pcs, 3000 pcs/reel	0805

#### Note

- MOQ: minimum order quantity

<b>ABSOLUTE MAXIMUM RATINGS</b> ( $T_{amb} = 25^{\circ}\text{C}$ , unless otherwise specified)				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Reverse voltage		$V_R$	5	V
Forward current		$I_F$	100	mA
Peak forward current	$t_p/T = 0.1$ , $t_p = 100\ \mu\text{s}$	$I_{FM}$	200	mA
Surge forward current	$t_p = 100\ \mu\text{s}$	$I_{FSM}$	1	A
Power dissipation		$P_V$	190	mW
Junction temperature		$T_j$	100	$^{\circ}\text{C}$
Operating temperature range		$T_{amb}$	-40 to +85	$^{\circ}\text{C}$
Storage temperature range		$T_{stg}$	-40 to +100	$^{\circ}\text{C}$
Soldering temperature	acc. figure 7, J-STD-020	$T_{sd}$	260	$^{\circ}\text{C}$
Thermal resistance junction/ambient	J-STD-051, leads 7 mm, soldered on PCB	$R_{thJA}$	270	K/W

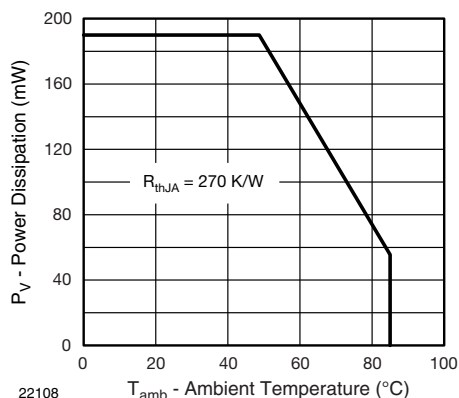


Fig. 1 - Power Dissipation Limit vs. Ambient Temperature

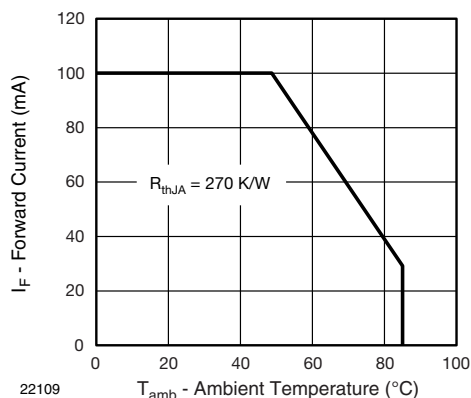


Fig. 2 - Forward Current Limit vs. Ambient Temperature

<b>BASIC CHARACTERISTICS</b> ( $T_{amb} = 25^{\circ}\text{C}$ , unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	$I_F = 100\ \text{mA}$ , $t_p = 20\ \text{ms}$	$V_F$		1.65	1.9	V
	$I_F = 1\ \text{A}$ , $t_p = 100\ \mu\text{s}$	$V_F$		2.9		V
Temperature coefficient of $V_F$	$I_F = 1\ \text{mA}$	$TK_{VF}$		-1.4		mV/K
	$I_F = 10\ \text{mA}$	$TK_{VF}$		-1.18		mV/K
Reverse current		$I_R$	not designed for reverse operation			$\mu\text{A}$
Junction capacitance	$V_R = 0\ \text{V}$ , $f = 1\ \text{MHz}$ , $E = 0\ \text{mW/cm}^2$	$C_J$		125		pF
Radiant intensity	$I_F = 100\ \text{mA}$ , $t_p = 20\ \text{ms}$	$I_e$	5	10	15	mW/sr
	$I_F = 1\ \text{A}$ , $t_p = 100\ \mu\text{s}$	$I_e$		85		mW/sr
Radiant power	$I_F = 100\ \text{mA}$ , $t_p = 20\ \text{ms}$	$\phi_e$		50		mW
Temperature coefficient of radiant power	$I_F = 100\ \text{mA}$	$TK_{\phi_e}$		-0.35		%/K
Angle of half intensity		$\varphi$		$\pm 60$		deg
Peak wavelength	$I_F = 100\ \text{mA}$	$\lambda_p$	840	850	870	nm
Spectral bandwidth	$I_F = 30\ \text{mA}$	$\Delta\lambda$		30		nm
Temperature coefficient of $\lambda_p$	$I_F = 30\ \text{mA}$	$TK_{\lambda_p}$		0.25		nm
Rise time	$I_F = 100\ \text{mA}$ , 20 % to 80 %	$t_r$		10		ns
Fall time	$I_F = 100\ \text{mA}$ , 20 % to 80 %	$t_f$		10		ns
Virtual source diameter		$d$		0.5		mm

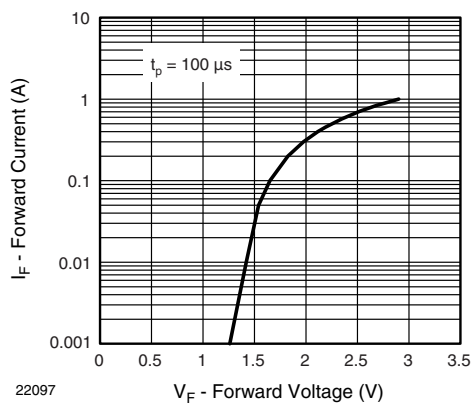
**BASIC CHARACTERISTICS** ( $T_{amb} = 25\text{ }^{\circ}\text{C}$ , unless otherwise specified)


Fig. 3 - Forward Current vs. Forward Voltage

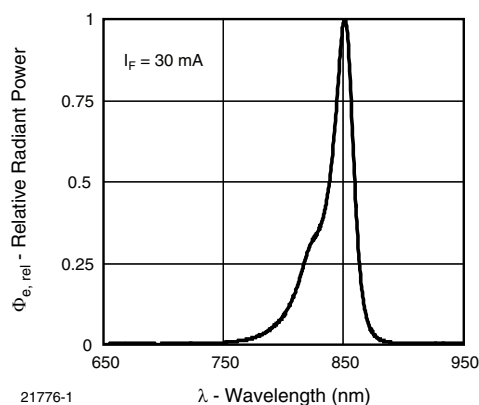


Fig. 5 - Relative Radiant Power vs. Wavelength

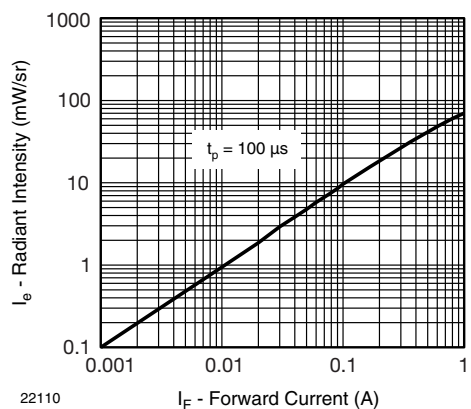


Fig. 4 - Radiant Intensity vs. Forward Current

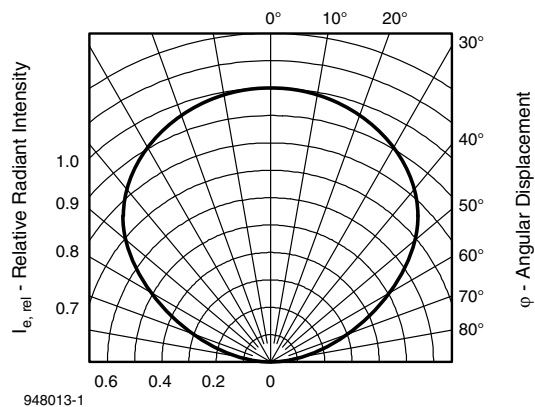


Fig. 6 - Relative Radiant Intensity vs. Angular Displacement

## REFLOW SOLDER PROFILE

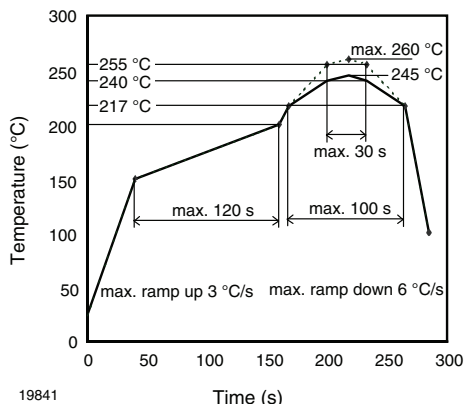
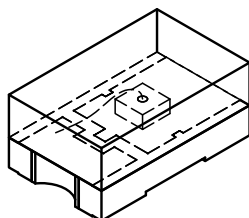
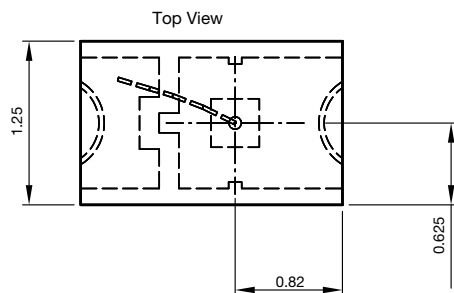
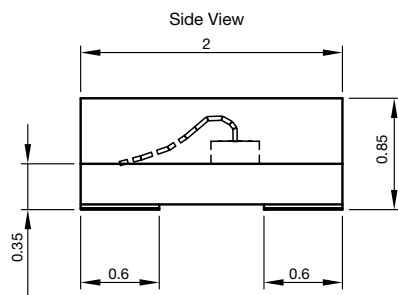
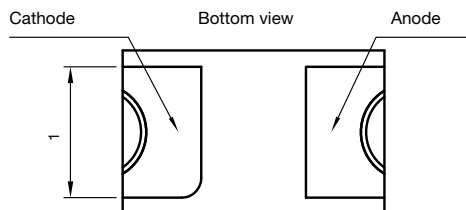
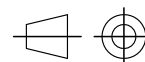


Fig. 7 - Lead (Pb)-free Reflow Solder Profile acc. J-STD-020

## PACKAGE DIMENSIONS in millimeters

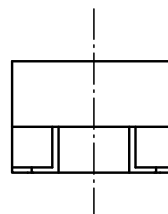


Drawing-No.: 6.541-5083.01-4  
Issue: 2; 10.09.2013

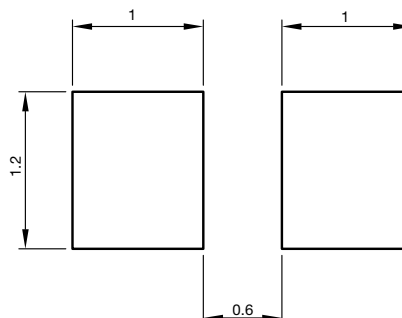


technical drawings  
specifications  
according to DIN

Not indicated tolerances  $\pm 0.1$



Recommended solder pad  
footprint



## DRYPACK

Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

## FLOOR LIFE

Time between soldering and removing from MBB must not exceed the time indicated in J-STD-020:

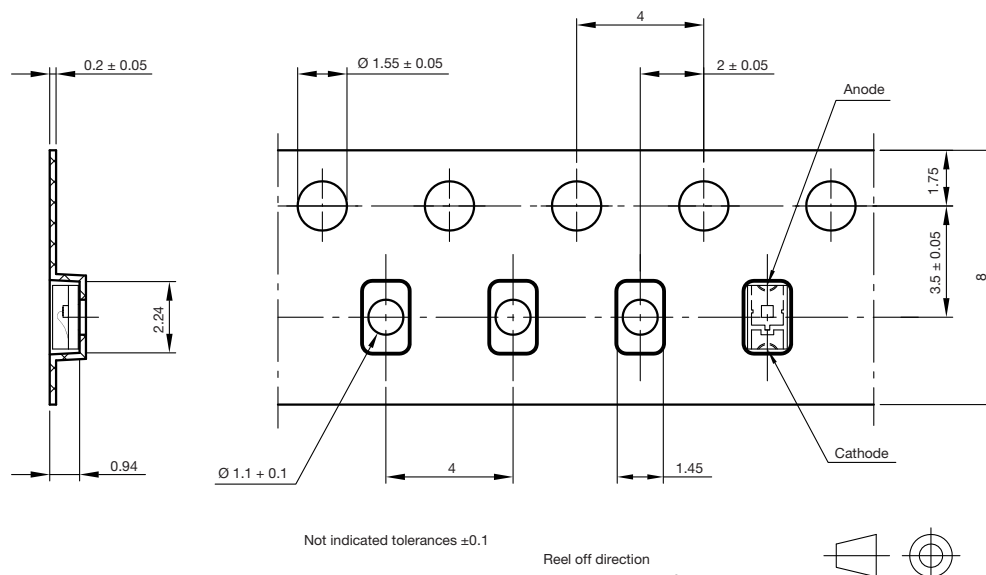
Moisture sensitivity: level 3

Floor life: 168 h

Conditions:  $T_{amb} < 30^{\circ}\text{C}$ ,  $RH < 60\%$

## DRYING

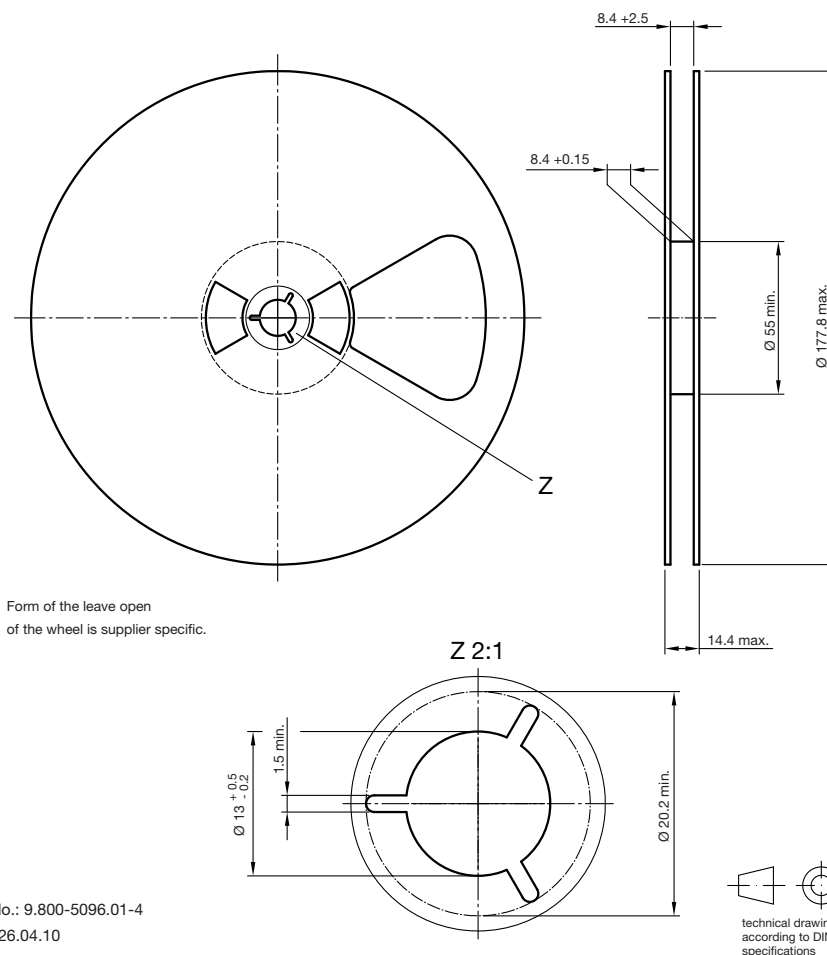
In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-020 or label. Devices taped on reel dry using recommended conditions 192 h at  $40^{\circ}\text{C}$  ( $+ 5^{\circ}\text{C}$ ),  $RH < 5\%$ .

**BLISTER TAPE DIMENSIONS** in millimeters


Drawing-No.: 9.700-5352.01-4

Issue: 1; 13.04.10

22112

**REEL DIMENSIONS** in millimeters


Drawing-No.: 9.800-5096.01-4

Issue: 2; 26.04.10

20875



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**Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.**

**Vishay Intertechnology, Inc. hereby certifies that all its products that are identified as Halogen-Free follow Halogen-Free requirements as per JEDEC JS709A standards. Please note that some Vishay documentation may still make reference to the IEC 61249-2-21 definition. We confirm that all the products identified as being compliant to IEC 61249-2-21 conform to JEDEC JS709A standards.**

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Minhang District, Shanghai , China

➤ Sales :

Direct     +86 (21) 6401-6692  
  
Email       amall@ameya360.com  
  
QQ         800077892  
  
Skype       ameyasales1 ameyasales2

➤ Customer Service :

Email       service@ameya360.com

➤ Partnership :

Tel         +86 (21) 64016692-8333  
  
Email       mkt@ameya360.com